

44.5mm (1.75INCH) SINGLE DIGIT NUMERIC DISPLAY

Part Number: SBA18-11SURKCGKWA

Hyper Red Green

Features

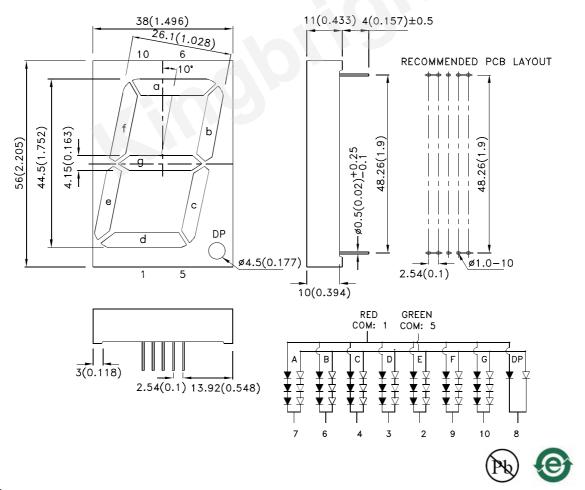
- 1.75 inch digit height.
- Low current operation.
- Excellent character appearance.
- Easy mounting on P.C. boards or sockets.
- Categorized for luminous intensity.
- Mechanically rugged.
- Standard : gray face, white segment.
- RoHS compliant.

Description

The Hyper Red source color devices are made with Al-GalnP on GaAs substrate Light Emitting Diode.

The Green source color devices are made with AlGaInP on GaAs substrate Light Emitting Diode.

Package Dimensions & Internal Circuit Diagram



Notes

- 1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted.
- 2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAH4193 REV NO: V.6A DATE: MAY/14/2013 PAGE: 1 OF 9
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: Q.M.CHEN ERP: 1301002496

Selection Guide

Part No.	Dice	Lens Type	lv (ucd) [1] @ 10mA		Description
			Min.	Тур.	-
SBA18-11SURKCGKWA	Hyper Red (AlGaInP)	- White Diffused	150000	310000	Common Anode ,Rt. Hand Decimal.
			*31000	*90000	
	Green (AlGaInP)		52000	140000	
			*21000	*50000	

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Hyper Red Green	645 574		nm	I==20mA
λD [1]	Dominant Wavelength	Hyper Red Green	630 570		nm	I=20mA
Δλ1/2	Spectral Line Half-width	Hyper Red Green	28 20		nm	I==20mA
С	Capacitance	Hyper Red Green	35 15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage (DP)	Hyper Red Green	5.85 (1.95) 6.3 (2.1)	7.5 (2.5) 7.5 (2.5)	V	I==20mA
lR	Reverse Current (Per Chip)	Hyper Red Green		10 (10)	uA	VR=5V

Notes:

- Navelength: +/-1nm.
 Forward Voltage: +/-0.1V.
 Wavelength value is traceable to the CIE127-2007 compliant national standards.

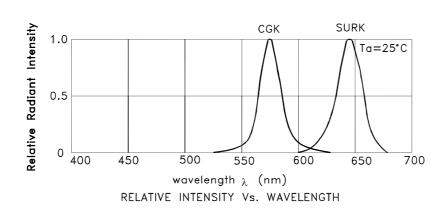
Absolute Maximum Ratings at TA=25°C

Parameter	Hyper Red	Green	Units
Power dissipation (DP)	225 (75)	225 (75)	mW
DC Forward Current (DP)	30 (30)	30 (30)	mA
Peak Forward Current [1] (DP)	185 (185)	150 (150)	mA
Reverse Voltage (Per Chip)	5 (5)		V
Operating/Storage Temperature			
Lead Solder Temperature [2]	260°C For 3-5 Seconds		

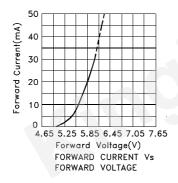
- 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
 2. 2mm below package base.

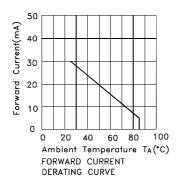
SPEC NO: DSAH4193 **REV NO: V.6A** DATE: MAY/14/2013 PAGE: 2 OF 9 APPROVED: WYNEC **CHECKED:** Joe Lee DRAWN: Q.M.CHEN ERP: 1301002496

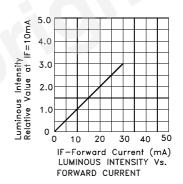
Luminous intensity/ luminous Flux: +/-15%.
 * Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

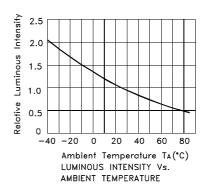


SBA18-11SURKCGKWA Hyper Red



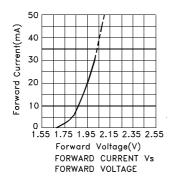


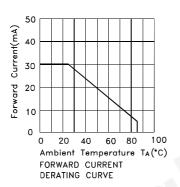


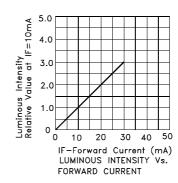


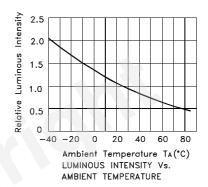
SPEC NO: DSAH4193 REV NO: V.6A
APPROVED: WYNEC CHECKED: Joe Lee

DATE: MAY/14/2013 DRAWN: Q.M.CHEN PAGE: 3 OF 9 ERP: 1301002496



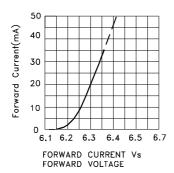


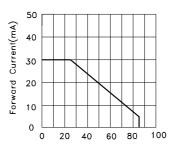




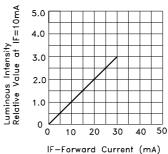
SPEC NO: DSAH4193 REV NO: V.6A DATE: MAY/14/2013 PAGE: 4 OF 9
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: Q.M.CHEN ERP: 1301002496

Green

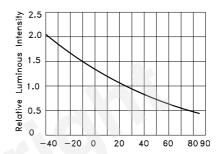




Ambient Temperature TA (°C) FORWARD CURRENT DERATING CURVE

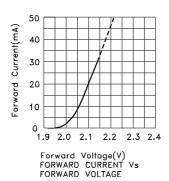


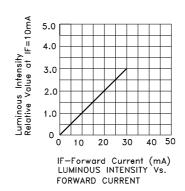


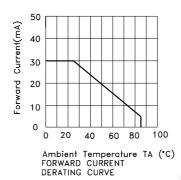


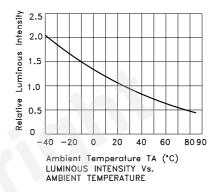
Ambient Temperature TA (°C) LUMINOUS INTENSITY Vs. AMBIENT TEMPERATURE

SPEC NO: DSAH4193 REV NO: V.6A DATE: MAY/14/2013 PAGE: 5 OF 9
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: Q.M.CHEN ERP: 1301002496





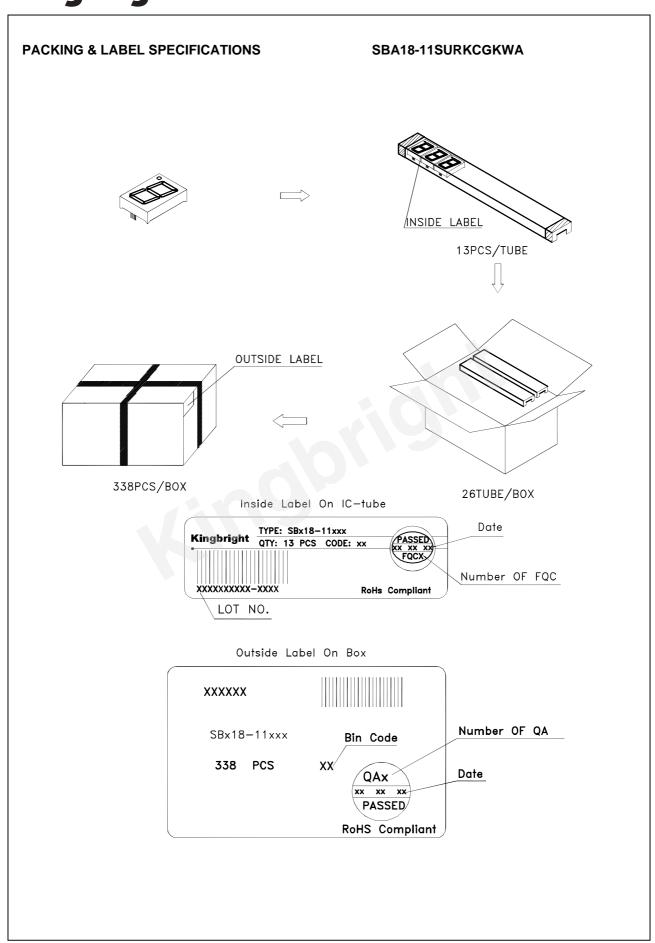




PAGE: 6 OF 9

ERP: 1301002496

SPEC NO: DSAH4193 REV NO: V.6A DATE: MAY/14/2013
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: Q.M.CHEN

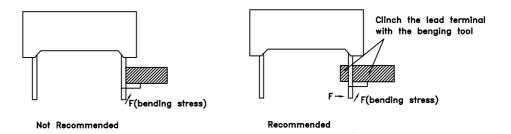


SPEC NO: DSAH4193 APPROVED: WYNEC REV NO: V.6A CHECKED: Joe Lee DATE: MAY/14/2013 DRAWN: Q.M.CHEN PAGE: 7 OF 9 ERP: 1301002496

THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

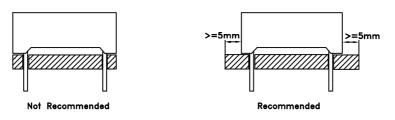


Installation

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



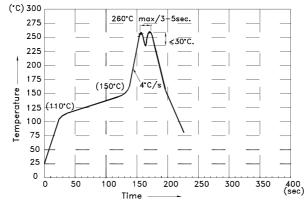
3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



SPEC NO: DSAH4193 APPROVED: WYNEC REV NO: V.6A CHECKED: Joe Lee DATE: MAY/14/2013 DRAWN: Q.M.CHEN PAGE: 8 OF 9 ERP: 1301002496

DISPLAY SOLDERING CONDITIONS

Wave Soldering Profile For Lead-free Through-hole LED.



NOTES:

- 1.Recommend the wave temperature 245°C~260°C.The maximum soldering temperature should be less than 260°C.
- 2.Do not apply stress on epoxy resins when temperature is over 85°C.
- 3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- 4.During wave soldering , the PCB top-surface temperature should be kept below 105°C
- 5.No more than once

Soldering General Notes:

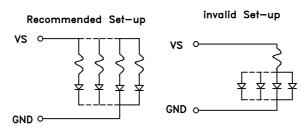
- 1. Through-hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

- 1.Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts .And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

- 1.Protective current-limiting resistors may be necessary to operate the Displays.
- 2.LEDs mounted in parallel should each be placed in series with its own current—limiting resistor.



Detailed application notes are listed on our website. http://www.kingbright.com/application notes

SPEC NO: DSAH4193 REV NO: V.6A DATE: MAY/14/2013 PAGE: 9 OF 9
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: Q.M.CHEN ERP: 1301002496