

QuickWorks

QuickLogic Corporation (Nasdaq:QUIK), the inventor and pioneer of Embedded Standard Products (ESPs), today announced the availability of version 9.5 of its QuickWorks(R) development software that provides enhanced support for its entire Eclipse-II family of Field Programmable Gate Arrays (FPGAs). In addition, the software accelerates time-to-market for customers through automatic migration from previous Eclipse designs to the new Eclipse II family. QuickWorks v9.5 also supports recent additions to the QuickMIPS ESP family -- the QL 902M, QL903M and QL904M devices.

QuickWorks v9.5 offers a comprehensive design environment for the Eclipse II family of FPGAs ranging from schematic and HDL-based design entry, HDL language editors and tutorials, logic synthesis, place and route, timing analysis, and simulation support. QuickWorks v9.5 also converts designs from the Eclipse family devices to their equivalent Eclipse-II counterparts, maintaining the same placement and routing.

"Finally, designers of battery operated and power critical systems have an ultra low power FPGA alternative to the high development cost of ASICs, and the feature-poor architecture of CPLDs," said Tim Saxe, VP of Engineering for QuickLogic. "QuickWorks v9.5 provides full support for this family including the new migration function that handles most of the mechanical, time consuming chores of moving from one technology to another."

Pricing and Availability

The basic QuickWorks v9.5 tool is available for no charge for Windows (98, 2000, ME, XP and NT) and UNIX platforms. The software can be downloaded from the QuickLogic website at www.quicklogic.com

QuickLogic Embedded Standard Product (ESP) Advantages

QuickLogic's ESP families offer standard product time-to-market and economics coupled with user customizable logic -- on the same piece of silicon. The performance, power, and cost advantages provided by these innovative devices and QuickLogic's ViaLink(R)-based logic architecture make these integrated circuits ideal for high-speed, low-power applications such as data and telecom systems, instrumentation and test systems. QuickLogic ESP families include QuickMIPS, QuickPCI(TM), QuickRAM(TM), QuickFC(TM) and QuickSD(TM).

About QuickLogic

QuickLogic Corporation (Nasdaq:QUIK) introduced the Embedded Standard Product (ESP) architecture in 1998, an innovation that delivers the guaranteed performance and lower cost of standard semiconductor products and the flexibility and time-to-market benefits of programmable logic. QuickLogic's proprietary ViaLink metal-to-metal interconnect technology offers high performance and is the foundation of the company's ESP families as well as our core

FPGA products. Founded in 1988, the company is located at 1277 Orleans Drive, Sunnyvale, CA 94089-1138. For more information please visit the QuickLogic web site at www.quicklogic.com

Safe Harbor Statement

This news release contains forward-looking statements based on current expectations that involve risks and uncertainties. QuickLogic's actual results may differ from the results described in the forward-looking statements. Factors that could cause actual results to differ include, but are not limited to, general conditions in the semiconductor industry, the time to market and other benefits of QuickWorks v9.5, and the performance of competitive products. Additional risk factors are detailed in QuickLogic's periodic reports and registration statements filed with the Securities and Exchange Commission.

Note to Editors: The QuickLogic name and logo and QuickWorks are registered trademarks and QuickPCI, QuickSD, QuickFC and QuickRAM are trademarks of QuickLogic Corporation. All other brands or trademarks are the property of their respective holders and should be treated as such.

Contacts

QuickLogic Corporation

Andrea Vedanayagam, 408/990-4000

andrea@quicklogic.com

Brian Faith, 408/990-4000

faith@quicklogic.com