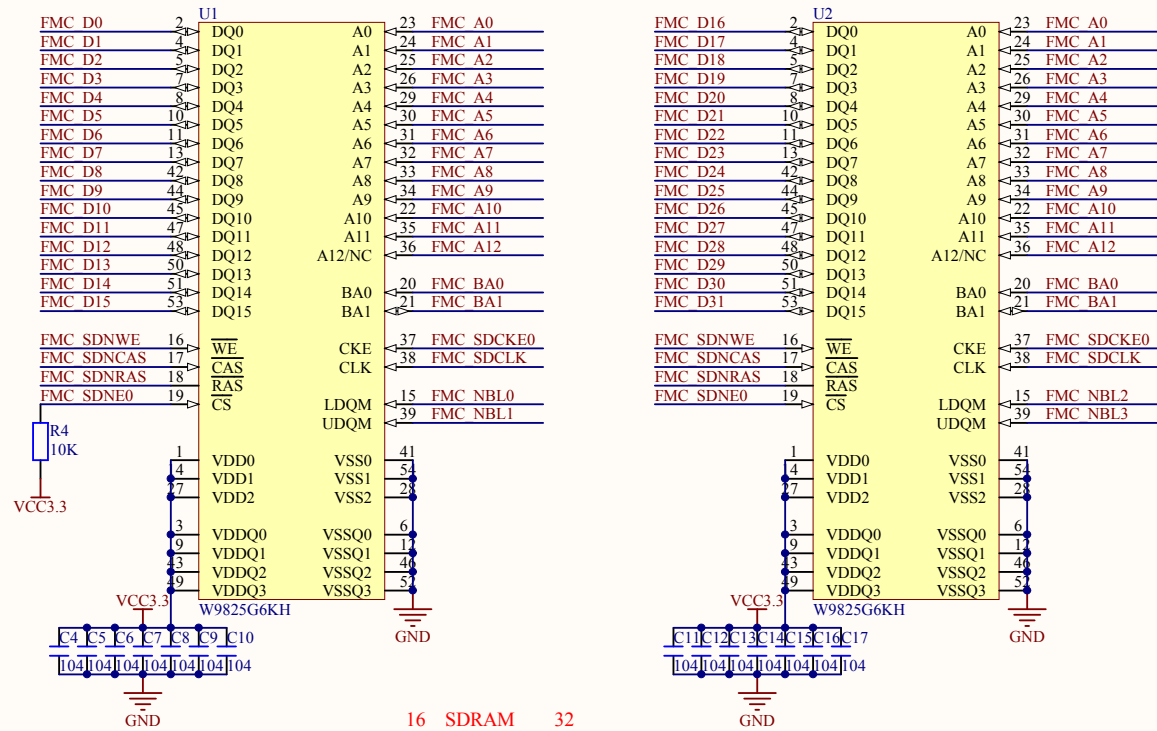
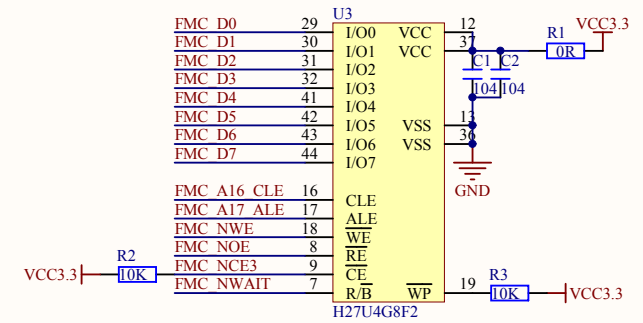


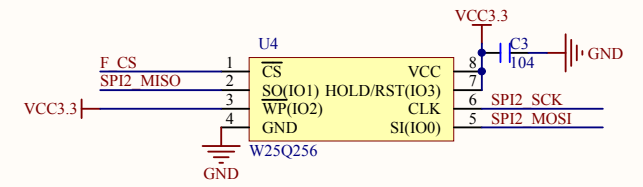
SDRAM



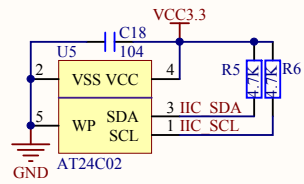
NAND FLASH



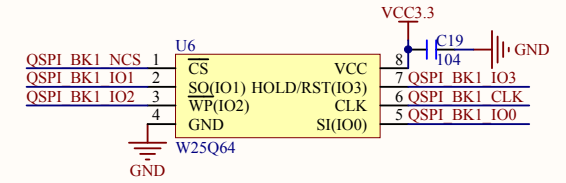
DATA FLASH



EEPROM



CODE FLASH



Title: ATK STM32F750 CORE BOARD	
Author: ALIENTEK	Size: A4
Date: 2019-06-24	File: STM32F750N8 MEMORY.SchDoc
Revision: *	Version: V1.2



MCU - GPIOA/B/C

U7B		
WK UP	PA0	N3
RMII REF CLK	PA1	N2
UART2 TX	ETH MDIO	PA2 P2
UART2 RX	PWM DAC	PA3 R2
DCMI HREF	STM DAC	PA4 N4
	STM ADC	PA5 P4
DCMI PCLK		PA6 P3
	RMII CS DV	PA7 R3
DCMI XCLK	REMOTE IN	PA8 F15
	USART1 TX	PA9 E15
	USART1 RX	PA10 D15
	USB D-	PA11 C15
	USB D+	PA12 B15
	JTMS	PA13 A15
	JTCK	PA14 A14
NRF CS	JTDI	PA15 A13
	LED1	PB0 R5
	LED0	PB1 R4
	QSPI BK1 CLK	PB2 M5
DCMI SDA	JTDO	PB3 A10
DCMI SCL	JTRST	PB4 A9
	LCD BL	PB5 A8
	QSPI BK1 NCS	PB6 B6
DCMI VSYNC		PB7 B5
DCMI D6		PB8 A7
DCMI D7		PB9 B4
	USART3 TX	PB10 P12
	USART3 RX	PB11 R13
NRF IRQ	GBC LED	PB12 L13
	SPI2 SCK	PB13 K14
	SPI2 MISO	PB14 R14
	SPI2 MOSI	PB15 R15
	FMC SDNWE	PC0 M2
	ETH MDC	PC1 M3
	FMC SDNE0	PC2 M4
	FMC SDCKE0	PC3 L4
	RMII RXD0	PC4 N5
	RMII RXD1	PC5 P5
	DCMI D0	PC6 H15
	DCMI D1	PC7 G15
SD1 D0	DCMI D2	PC8 G14
SD1 D1	DCMI D3	PC9 F14
SD1 D2		PC10 B14
SD1 D3	DCMI D4	PC11 B13
SD1 CLK		PC12 A12
	KEY2	PC13 D1
	PA0/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/USART2_CTS/UART4_TX/SAI2_SD_B/ETH_MII_CRS/ADC123_IN0/WKUP1	
	PA1/TIM2_CH2/TIM5_CH2/USART2_RTS/UART4_RX/QUADSPI_BK1_I03/SAI2_MCK_B/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/LCD_R2/ADC123_IN1	
	PA2/TIM2_CH3/TIM5_CH3/TIM9_CH1/USART2_TX/SAI2_SCK_B/ETH_MDIO/LCD_R1/ADC123_IN2/WKUP2	
	PA3/TIM2_CH4/TIM5_CH4/TIM9_CH2/USART2_RX/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC123_IN3	
	PA4/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/USART2_CK/OTG_HS_SOF/DCMI_HSYN/LCD_VSYN/ADC12_IN4/DAC_OUT1	
	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/I2S1_CK/OTG_HS_ULPI_CK/LCD_R4/ADC12_IN5/DAC_OUT2	
	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SPI1_MISO/TIM13_CH1/DCMI_PIXCLK/LCD_G2/ADC12_IN6	
	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/I2S1_SD/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_SDNWE/ADC12_IN7	
	PA8/MCO1/TIM1_CH1/TIM8_BKIN/I2C3_SCL/USART1_CK/OTG_FS_SOF/LCD_R6	
	PA9/TIM1_CH2/I2C3_SMB/SAI2_SCK/I2S2_CK/USART1_TX/DCMI_D0/OTG_FS_VBUS	
	PA10/TIM1_CH3/USART1_RX/OTG_FS_ID/DCMI_D1	
	PA11/TIM1_CH4/USART1_CTS/CAN1_RX/OTG_FS_DM/LCD_R4	
	PA12/TIM1_ETR/USART1_RTS/SAI2_FS_B/CAN1_TX/OTG_FS_DP/LCD_R5	
	PA13/JTMS	
	PA14/JTCK	
	PA15/JTDI/TIM2_CH1/TIM2_ETR/HDMI_CEC/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/UART4_RTS	
	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/UART4_CTS/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/ADC12_IN8	
	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/ADC12_IN9	
	PB2/SAI1_SD_A/SPI3_MOSI/I2S3_SD/QUADSPI_CLK	
	PB3/JTDO/TIM2_CH2/SPI1_SCK/I2S1_CK/SPI3_SCK/I2S3_CK	
	PB4/NJTRST/TIM3_CH1/SPI1_MISO/SPI3_MISO/SPI2_NSS/I2S2_WS	
	PB5/TIM3_CH2/I2C1_SMB/SAI1_MOSI/I2S1_SD/SPI3_MOSI/I2S3_SD/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10	
	PB6/TIM4_CH1/HDMI-CEC/I2C1_SCL/USART1_TX/CAN2_TX/QUADSPI_BK1_NCS/FMC_SDNWE1/DCMI_D5	
	PB7/TIM4_CH2/I2C1_SDA/USART1_RX/FMC_NL/DCMI_VSYNC	
	PB8/TIM4_CH3/TIM10_CH1/I2C1_SCL/CAN1_RX/ETH_MII_TXD3/SDMMC1_D4/DCMI_D6/LCD_B6	
	PB9/TIM4_CH4/TIM11_CH1/I2C1_SDA/SPI2_NSS/I2S2_WS/CAN1_TX/SDMMC1_D5/DCMI_D7/LCD_B7	
	PB10/TIM2_CH3/I2C2_SCL/SPI2_SCK/I2S2_CK/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4	
	PB11/TIM2_CH4/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5	
	PB12/TIM1_BKIN/I2C2_SMB/SAI2_NSS/I2S2_WS/USART3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID	
	PB13/TIM1_CH1N/SPI2_SCK/I2S2_CK/USART3_CTS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_RXD1/OTG_HS_VBUS	
	PB14/TIM1_CH2N/TIM8_CH2N/SPI2_MISO/USART3_RTS/TIM12_CH1/OTG_HS_DM	
	PB15/RTC_REFIN/TIM1_CH3N/TIM8_CH3N/SPI2_MOSI/I2S2_SD/TIM12_CH2/OTG_HS_DP	
	PC0/SAI2_FS_B/OTG_HS_ULPI_STP/FMC_SDNWE/LCD_R5/ADC123_IN10	
	PC1/SPI2_MOSI/I2S2_SD/SAI1_SD_A/ETH_MDC/ADC123_IN11/RTC_RAMP3/WKUP3	
	PC2/SPI2_MISO/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNWE0/ADC123_IN12	
	PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC123_IN13	
	PC4/I2S1_MCK/SPDIFRX_IN2/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_SDNWE0/ADC12_IN14	
	PC5/SPDIFRX_IN3/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_SDCKE0/ADC12_IN15	
	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/USART6_TX/SDMMC1_D6/DCMI_D0/LCD_HSYN	
	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/USART6_RX/SDMMC1_D7/DCMI_D1/LCD_G6	
	PC8/TIM3_CH3/TIM8_CH3/UART5_RTS/USART6_CK/SDMMC1_D0/DCMI_D2	
	PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_I00/SDMMC1_D1/DCMI_D3	
	PC10/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/QUADSPI_BK1_I01/SDMMC1_D2/DCMI_D8/LCD_R2	
	PC11/SPI3_MISO/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SDMMC1_D3/DCMI_D4	
	PC12/SPI3_MOSI/I2S3_SD/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9	
	PC13/RTC_TAMP1/RTC_TS/RTC_OUT/WKUP4	

STM32F750N8H6

Title: ATK STM32F750 CORE BOARD	
Author: ALIENTEK	Size: A4
Date: 2019-06-24	File: STM32F750N8_PIN_ABC.SchDoc
Revision: *	Version: V1.2



MCU - GPIOD/E/F

U7C

FMC D2	PD0	B12	PD0/CAN1_RX/FMC_D2
FMC D3	PD1	C12	PD1/CAN1_TX/FMC_D3
SD1_CMD	PD2	D12	PD2/TIM3_ETR/UART5_RX/SDMMC1_CMD/DCMI_D11
DCMI D5	PD3	C11	PD3/SPI2_SCK/I2S2_CK/USART2_CTS/FMC_CLK/DCMI_D5/LCD_G7
FMC NOE	PD4	D11	PD4/USART2_RTS/FMC_NOE
FMC NWE	PD5	C10	PD5/USART2_TX/FMC_NWE
FMC NWAIT	PD6	B11	PD6/SPI3_MOSI/I2S3_SD/SAI1_SD_A/USART2_RX/FMC_NWAIT/DCMI_D10/LCD_B2
FMC NE1	PD7	A11	PD7/USART2_CK/SPDIFRX_IN0/FMC_NE1
FMC D13	PD8	L15	PD8/USART3_TX/SPDIFRX_IN11/FMC_D13
FMC D14	PD9	L14	PD9/USART3_RX/FMC_D14
FMC D15	PD10	K15	PD10/USART3_CK/FMC_D15/LCD_B3
FMC A16_CLE	PD11	N10	PD11/I2C4_SMBA/USART3_CTS/QUADSPI_BK1_IO0/SAI2_SD_A/FMC_D16/FMC_CLE
FMC A17_ALE	PD12	M10	PD12/TIM4_CH1/LPTIM1_IN1/I2C4_SCL/USART3_RTS/QUADSPI_BK1_IO1/SAI2_FS_A/FMC_A17/FMC_ALE
FMC A18	PD13	M11	PD13/TIM4_CH2/LPTIM1_OUT/I2C4_SDA/QUADSPI_BK1_IO3/SAI2_SCK_A/FMC_A18
FMC D0	PD14	L12	PD14/TIM4_CH3/UART8_CTS/FMC_D0
FMC D1	PD15	K13	PD15/TIM4_CH4/UART8_RTS/FMC_D1
FMC NBL0	PE0	A6	PE0/TIM4_ETR/LPTIM1_ETR/UART8_RX/SAI2_MCK_A/FMC_NBL0/DCMI_D2
FMC NBL1	PE1	A5	PE1/LPTIM1_IN2/UART8_TX/FMC_NBL1/DCMI_D3
SAI1_MCLKA	PE2	A3	PE2/SPI4_SCK/SAI1_MCLK_A/QUADSPI_BK1_IO2/ETH_MII_TXD3/FMC_A23
SAI1_SDB	PE3	A2	PE3/SAI1_SD_B/FMC_A19
SAI1_FSA	PE4	A1	PE4/SPI4_NSS/SAI1_FS_A/FMC_A20/DCMI_D4/LCD_B0
SAI1_SCKA	PE5	B1	PE5/TIM9_CH1/SPI4_MISO/SAI1_SCK_A/FMC_A21/DCMI_D6/LCD_G0
SAI1_SDA	PE6	B2	PE6/TIM1_BKIN2/TIM9_CH2/SPI4_MOSI/SAI1_SD_A/SAI2_MCK_B/FMC_A22/DCMI_D7/LCD_G1
FMC D4	PE7	R8	PE7/TIM1_ETR/UART7_RX/QUADSPI_BK2_IO0/FMC_D4
FMC D5	PE8	N9	PE8/TIM1_CH1/UART7_TX/QUADSPI_BK2_IO1/FMC_D5
FMC D6	PE9	P9	PE9/TIM1_CH1/UART7_RTS/QUADSPI_BK2_IO2/FMC_D6
FMC D7	PE10	R9	PE10/TIM1_CH2N/UART7_CTS/QUADSPI_BK2_IO3/FMC_D7
FMC D8	PE11	P10	PE11/TIM1_CH2/SPI4_NSS/SAI2_SD_B/FMC_D8/LCD_G3
FMC D9	PE12	R10	PE12/TIM1_CH3N/SPI4_SCK/SAI2_SCK_B/FMC_D9/LCD_B4
FMC D10	PE13	R12	PE13/TIM1_CH3/SPI4_MISO/SAI2_FS_B/FMC_D10/LCD_DE
FMC D11	PE14	P11	PE14/TIM1_CH4/SPI4_MOSI/SAI2_MCK_B/FMC_D11/LCD_CLK
FMC D12	PE15	R11	PE15/TIM1_BKIN/FMC_D12/LCD_R7
FMC A0	PF0	D2	PF0/I2C2_SDA/FMC_A0
FMC A1	PF1	E2	PF1/I2C2_SCL/FMC_A1
FMC A2	PF2	G2	PF2/I2C2_SMBA/FMC_A2
FMC A3	PF3	H2	PF3/FMC_A3/ADC3_IN9
FMC A4	PF4	J2	PF4/FMC_A4/ADC3_IN14
FMC A5	PF5	K3	PF5/FMC_A5/ADC3_IN15
QSPI BK1 IO3	PF6	K2	PF6/TIM10_CH1/SPI5_NSS/SAI1_SD_B/UART7_RX/QUADSPI_BK1_IO03/ADC3_IN4
QSPI BK1 IO2	PF7	K1	PF7/TIM11_CH1/SPI5_SCK/SAI1_MCLK_B/UART7_TX/QUADSPI_BK1_IO02/ADC3_IN5
QSPI BK1 IO0	PF8	L3	PF8/SPI5_MISO/SAI1_SCK_B/UART7_RTS/TIM13_CH1/QUADSPI_BK1_IO0/ADC_IN6
QSPI BK1 IO1	PF9	L2	PF9/SPI5_MOSI/SAI1_FS_B/UART7_CTS/TIM14_CH1/QUADSPI_BK1_IO1/ADC3_IN7
F_CS	PF10	L1	PF10/DCMI_D11/LCD_DE/ADC3_IN8
FMC SDNRAS	PF11	P8	PF11/SPI5_MOSI/SAI2_SD_B/FMC_SDNRAS/DCMI_D12
FMC A6	PF12	M6	PF12/FMC_A6
FMC A7	PF13	N6	PF13/I2C4_SMBA/FMC_A7
FMC A8	PF14	P6	PF14/I2C4_SCL/FMC_A8
FMC A9	PF15	M8	PF15/I2C4_SDA/FMC_A9

STM32F750N8H6

Title: ATK STM32F750 CORE BOARD	
Author: ALIENTEK	Size: A4
Date: 2019-06-24	File: STM32F750N8_PIN_DEF.SchDoc
Revision: *	Version: V1.2



正点原子

MCU - GPIOG/H/I/J/K

U7D

FMC A10	PG0	N7	PG0/FMC_A10
FMC A11	PG1	M7	PG1/FMC_A11
FMC A12	PG2	M13	PG2/FMC_A12
T MISO	PG3	M12	PG3/FMC_A13
FMC BA0	PG4	N12	PG4/FMC_A14/FMC_BA0
FMC BA1	PG5	N11	PG5/FMC_A15/FMC_BA1
NRF CE	PG6	J15	PG6/DCMI_D12/LCD_R7
T MOSI	PG7	J14	PG7/USART6_CK/FMC_INT/DCMI_D13/LCD_CLK
FMC SDCLK	PG8	H14	PG8/SPI6_NSS/SPIDIFRX_IN2/USART6_RTS/ETH_PPS_OUT/FMC_SDCLK
FMC NCE3	PG9	D9	PG9/SPDIFRX_IN3/USART6_RX/QUADSPI_BK2_IO2/SAI2_FS_B/FMC_NE2/FMC_NCE/DCMI_VSYNC
I2C INT	PG10	C8	PG10/LCD_G3/SAI2_SD_B/FMC_NE3/DCMI_D2/LCD_B2
RMII TX EN	PG11	B8	PG11/SPDIFRX_IN0/ETH_MII_TX_EN/ETH_RMII_TX_EN/DCMI_D3/LCD_B3
DCMI RESET	PG12	C7	PG12/LPTIM1_IN1/SPI6_MISO/SPDIFRX_IN1/USART6_RTS/LCD_B4/FMC_NE4/LCD_B1
RMII TXD0	PG13	B3	PG13/LPTIM1_OUT/SPI6_SCK/USART6_CTS/ETH_MII_TXD0/ETH_RMII_TXD0/FMC_A24/LCD_R0
RMII TXD1	PG14	A4	PG14/LPTIM1_ETR/SPI6_MOSI/USART6_TX/QUADSPI_BK2_IO3/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_A25/LCD_B0
FMC SDNCAS	PG15	B7	PG15/USART6_CTS/FMC_SDNCAS/DCMI_D13
KEY1	PH2	K4	PH2/LPTIM1_IN2/QUADSPI_BK2_IO0/SAI2_SCK_B/ETH_MII_CRS/FMC_SDCKE0/LCD_R0
KEY0	PH3	J4	PH3/QUADSPI_BK2_IO1/SAI2_MCK_B/ETH_MII_COL/FMC_SDNE0/LCD_R1
IIC_SCL	PH4	H4	PH4/I2C2_SCL/OTG_HS_ULPI_NXT
IIC_SDA	PH5	J3	PH5/I2C2_SDA/SPI5_NSS/FMC_SDNWE
T_SCK	PH6	P13	PH6/I2C2_SMB/SAI2_SCK/TIM12_CH1/ETH_MII_RXD2/FMC_SDNE1/DCMI_D8
T_PEN	PH7	N13	PH7/I2C3_SCL/SPI5_MISO/ETH_MII_RXD3/FMC_SDCKE1/DCMI_D9
FMC D16	PH8	P14	PH8/I2C3_SDA/FMC_D16/DCMI_HSYNC/LCD_R2
FMC D17	PH9	N14	PH9/I2C3_SMB/SAI2_CH2/FMC_D17/DCMI_D0/LCD_R3
FMC D18	PH10	P15	PH10/TIM5_CH1/I2C4_SMB/SAI2_CH1/DCMI_D1/LCD_R4
FMC D19	PH11	N15	PH11/TIM5_CH2/I2C4_SCL/FMC_D19/DCMI_D2/LCD_R5
FMC D20	PH12	M15	PH12/TIM5_CH3/I2C4_SDA/FMC_D20/DCMI_D3/LCD_R6
FMC D21	PH13	E12	PH13/TIM8_CH1/CAN1_TX/FMC_D21/LCD_G3
FMC D22	PH14	E13	PH14/TIM8_CH2/FMC_D22/DCMI_D4/LCD_G3
FMC D23	PH15	D13	PH15/TIM8_CH3/FMC_D23/DCMI_D11/LCD_G4
FMC D24	P10	E14	P10/TIM5_CH4/SPI2_NSS/I2S2_WS/FMC_D24/DCMI_D13/LCD_G5
FMC D25	P11	D14	P11/TIM8_BKIN2/SPI2_SCK/I2S2_CK/FMC_D25/DCMI_D8/LCD_G6
FMC D26	P12	C14	P12/TIM8_CH4/SPI2_MISO/FMC_D26/DCMI_D9/LCD_G7
FMC D27	P13	C13	P13/TIM8_ETR/SPI2_MOSI/I2S2_SD/FMC_D27/DCMI_D10
FMC NBL2	P14	C3	P14/TIM8_BKIN/SAI2_MCK_A/FMC_NBL2/DCMI_D5/LCD_B4
FMC NBL3	P15	D3	P15/TIM8_CH1/SAI2_SCK_A/FMC_NBL3/DCMI_VSYNC/LCD_B5
FMC D28	P16	D6	P16/TIM8_CH2/SAI2_SD_A/FMC_D28/DCMI_D6/LCD_B6
FMC D29	P17	D4	P17/TIM8_CH3/SAI2_FS_A/FMC_D29/DCMI_D7/LCD_B7
T_CS	P18	C2	P18/RTC_TAMP2/RTC_TS/WKUP5
FMC D30	P19	E4	P19/CAN1_RX/FMC_D30/LCD_VSYNC
FMC D31	P110	D5	P110/ETH_MII_RX_ER/FMC_D31/LCD_HSYNC
GBC_KEY	P111	E3	P111/OTG_HS_ULPI_DIR/WKUP6
LCD_HSYNC	P112	E3	P112/LCD_HSYNC
LCD_VSYNC	P113	G3	P113/LCD_VSYNC
LCD_CLK	P114	H3	P114/LCD_CLK
LCD_R0	P115	G4	P115/LCD_R0
R6	PJ0	LCD_R1	PJ0/LCD_R1
R7	PJ1	LCD_R2	PJ1/LCD_R2
P7	PJ2	LCD_R3	PJ2/LCD_R3
N8	PJ3	LCD_R4	PJ3/LCD_R4
M9	PJ4	LCD_R5	PJ4/LCD_R5
M14	PJ5	LCD_R6	PJ5/LCD_R6
K12	PJ6	LCD_R7	PJ6/LCD_R7
J12	PJ7	LCD_G0	PJ7/LCD_G0
H12	PJ8	LCD_G1	PJ8/LCD_G1
J13	PJ9	LCD_G2	PJ9/LCD_G2
H13	PJ10	LCD_G3	PJ10/LCD_G3
G12	PJ11	LCD_G4	PJ11/LCD_G4
B10	PJ12	LCD_B0	PJ12/LCD_B0
B9	PJ13	LCD_B1	PJ13/LCD_B1
C9	PJ14	LCD_B2	PJ14/LCD_B2
D10	PJ15	LCD_B3	PJ15/LCD_B3
G13	PK0	LCD_G5	PK0/LCD_G5
F12	PK1	LCD_G6	PK1/LCD_G6
F13	PK2	LCD_G7	PK2/LCD_G7
D8	PK3	LCD_B4	PK3/LCD_B4
D7	PK4	LCD_B5	PK4/LCD_B5
C6	PK5	LCD_B6	PK5/LCD_B6
C5	PK6	LCD_B7	PK6/LCD_B7
C4	PK7	LCD_DE	PK7/LCD_DE

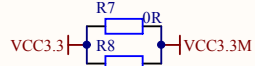
STM32F750N8H6

Title: ATK STM32F750 CORE BOARD	
Author: ALIENTEK	Size: A4
Date: 2019-06-24	File: STM32F750N8_PIN_GHIJK_SchDoc
Revision: *	Version: V1.2

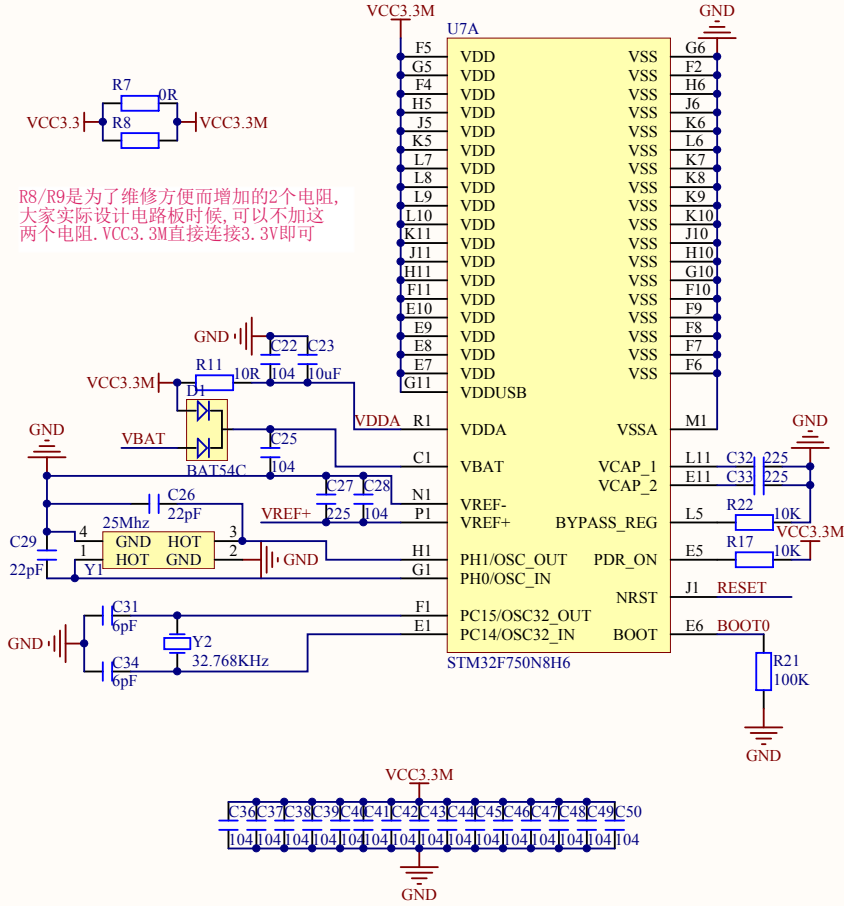


正点原子

MCU - POWER

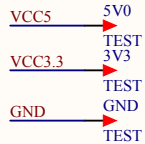


R8/R9是为了维修方便而增加的2个电阻,大家实际设计电路板时候,可以不加这两个电阻. VCC3.3M直接连接3.3V即可

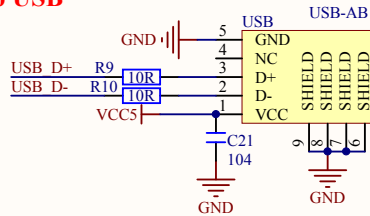


TEST POINT

这是三个电源电压测试点。用来测试核心板的电源是否正常。也可以用来给核心板供电：焊接GND和5V，然后接外部5V电压即可。



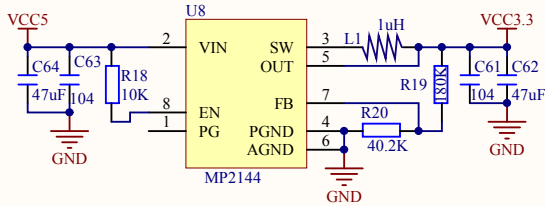
Micro USB



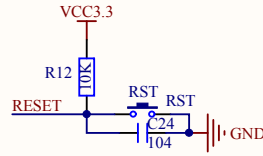
此MicroUSB接口有如下功能：
1, 单独使用核心板时，可给核心板供电。
2, 可做USB Slave接口，连接电脑，同时也可以供电。
3, 可做USB Host接口（需MicroUSB转OTG线），接U盘等。

注意：通过主板上的P5可以控制VREF+。

DC-DC POWER



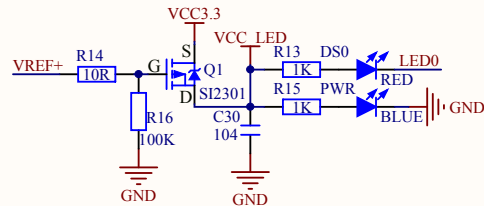
RESET



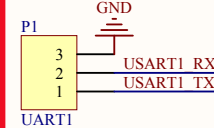
KEY



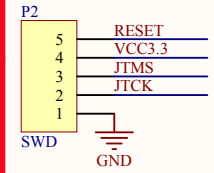
LED



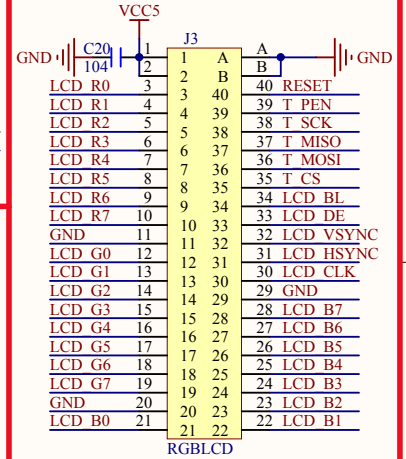
USART1



SWD



RGBLCD



MOTHER BOARD CON

PA6	J2	30	31	VREF+	VCC5	J1	30	31	GND
PA4		29	32	PD5	VCC5		29	32	GND
PA5		28	33	PD4	VBAT		28	33	PH15
PH4		27	34	PD7	PB8		27	34	PJ0
PH5		26	35	PA3	PB9		26	35	PJ1
PB1		25	36	PC1	PB7		25	36	PJ2
PB0		24	37	PC4	PE2		24	37	PJ3
PH3		23	38	PC5	PB3		23	38	PJ4
PH2		22	39	PA2	PB4		22	39	PJ5
PC13		21	40	PA7	PG10		21	40	PJ6
PE3		20	41	PA1	PG14		20	41	PJ7
PE4		19	42	PI11	PG13		19	42	PJ8
PE6		18	43	PB13	PG11		18	43	PJ9
PE5		17	44	PB14	PG12		17	44	PJ10
PD14		16	45	PB12	BOOT0		16	45	PJ11
PD15		15	46	PB15	PD3		15	46	PK0
PD0		14	47	PB11	PD2		14	47	PK1
PD1		13	48	PB10	PC8		13	48	PK2
PE7		12	49	PD13	PC12		12	49	PK12
PE8		11	50	PC7	PC9		11	50	PK13
PE9		10	51	PC6	PC10		10	51	PK14
PE10		9	52	PG6	PC11		9	52	PK15
PE11		8	53	PA8	PB5		8	53	PK3
PE12		7	54	PA12	PI8		7	54	PK4
PE13		6	55	PA11	PG7		6	55	PK5
PE14		5	56	PA10	PG3		5	56	PK6
PE15		4	57	PA9	PH6		4	57	PK7
PD8		3	58	PA13	PH7		3	58	PK14
PD9		2	59	PA14	PA0		2	59	PK13
PD10		1	60	PA15	RESET		1	60	PK12

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Version: V1.2



52.00mm

42.00mm

