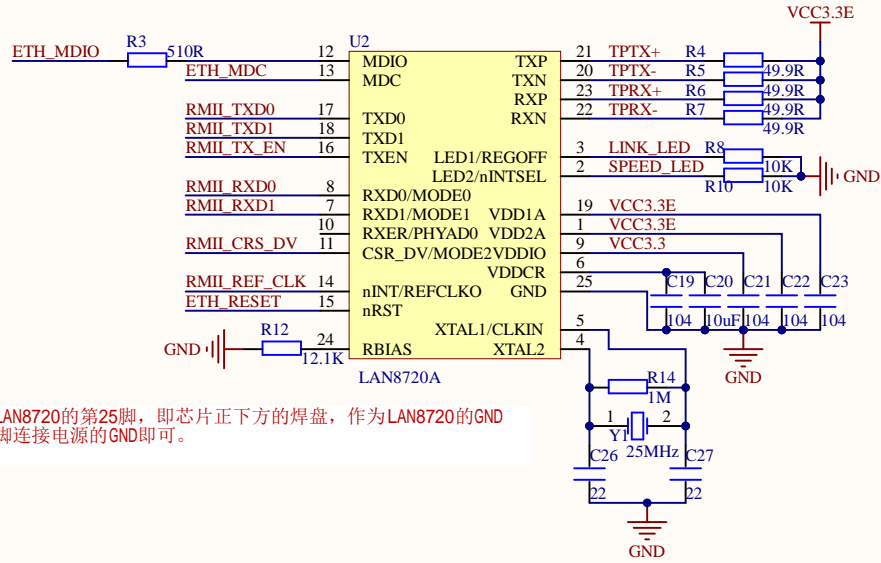
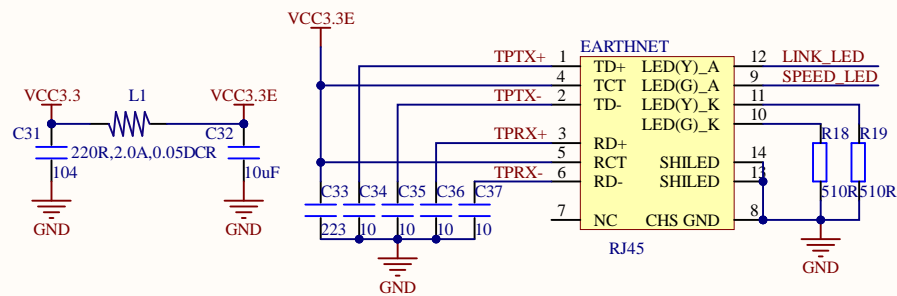


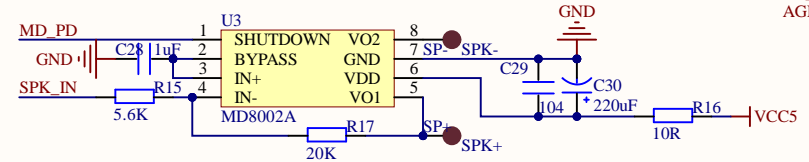
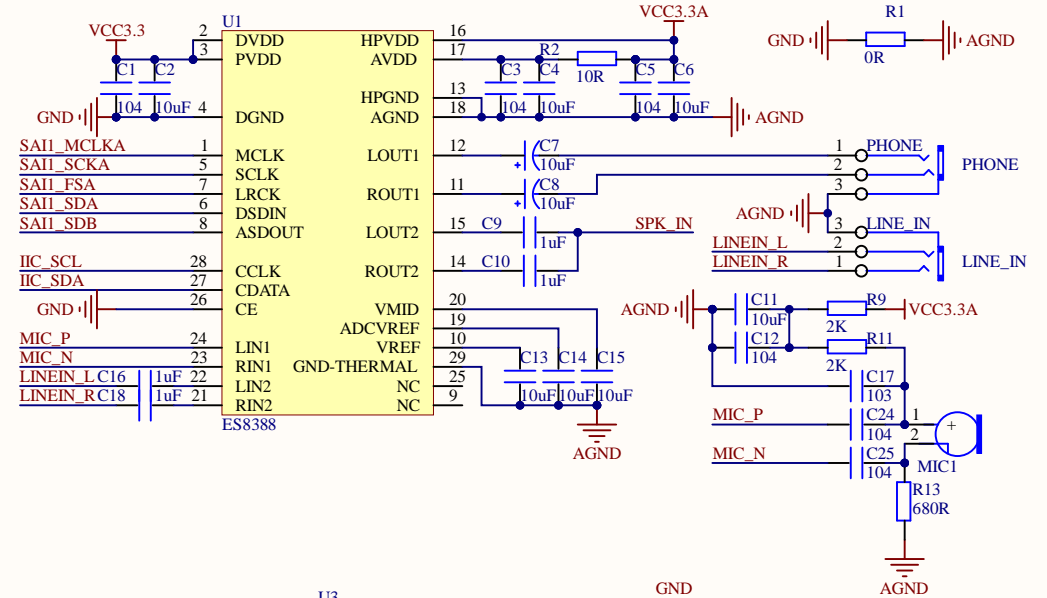
EARTHNET



LAN8720的第25脚，即芯片正下方的焊盘，作为LAN8720的GND脚连接电源的GND即可。

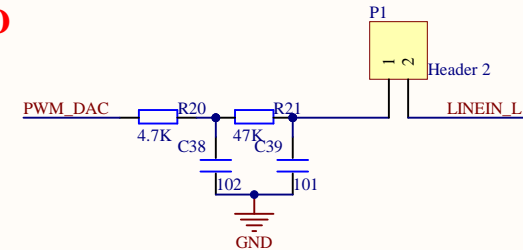


I2S DAC&ADC



SPK+和SPK-连接开发板自带的喇叭，在开发板底部。

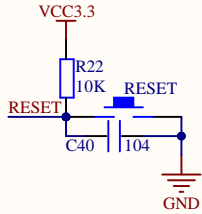
PWM DAC/AUDIO



Title: AUDIOÐNET.SchDoc	
Project: STM32_MOTHERBOARD_V1.2.PrjPcb	
Size: A4	Author: lycreturn@ALIENTEK
Date: 2021/11/12	Version: V1.0
Sheet: * of *	



RESET

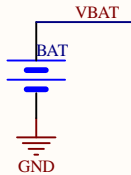


CORE BOARD

LCD_HSYNC	PI12	J1_60	PI12	RESET	J1_1	RESET
LCD_VSYNC	PI13	J1_59	PI13	PA0	J1_2	PA0
LCD_CLK	PI14	J1_58	PI13	J1_3	PH7	T_PEN
LCD_DE	PK7	J1_57	PK7	J1_4	PH6	T_SCK
LCD_B7	PK6	J1_56	PK6	J1_5	PG3	T_MISO
LCD_B6	PK5	J1_55	PK6	J1_6	PG7	T_MOSI
LCD_B5	PK4	J1_54	PK5	J1_7	P18	T_CS
LCD_B4	PK3	J1_53	PK4	J1_8	PB5	LCD_BL
LCD_B3	PJ15	J1_52	PJ15	J1_9	PC11	SD1_D3
LCD_B2	PJ14	J1_51	PJ14	J1_10	PC10	SD1_D2
LCD_B1	PJ13	J1_50	PJ14	J1_11	PC9	SD1_D1
LCD_B0	PI12	J1_49	PJ13	J1_12	PC12	SD1_CLK
LCD_G7	PK2	J1_48	PJ12	J1_13	PC8	SD1_D0
LCD_G6	PK1	J1_47	PK1	J1_14	PD2	SD1_CMD
LCD_G5	PK0	J1_46	PK1	J1_15	PD3	
LCD_G4	PI11	J1_45	PK0	J1_16	BOOT0	
LCD_G3	PI10	J1_44	PJ11	J1_17	PG12	SPDIF_RX
LCD_G2	PJ9	J1_43	PJ10	J1_18	PG11	RMII_TX_EN
LCD_G1	PJ8	J1_42	PJ9	J1_19	PG13	RMII_TXD0
LCD_G0	PJ7	J1_41	PJ8	J1_20	PG14	RMII_TXD1
LCD_R7	PJ6	J1_40	PJ7	J1_21	PG10	1WIRE_DQ
LCD_R6	PJ5	J1_39	PJ6	J1_22	PB4	JTRST
LCD_R5	PJ4	J1_38	PJ5	J1_23	PB3	JTDO
LCD_R4	PJ3	J1_37	PJ4	J1_24	PE2	SAII_MCLKA
LCD_R3	PJ2	J1_36	PJ3	J1_25	PB7	
LCD_R2	PJ1	J1_35	PJ2	J1_26	PB9	
LCD_R1	PJ0	J1_34	PJ1	J1_27	PB8	
LCD_R0	PI15	J1_33	PJ0	J1_28	VBAT	
			J1_32	J1_29		
			J1_31	J1_30		

NRF_CS	JTDI	PA15	J2_60	PA15	PD10	J2_1	PD10	FMC_D15
	JTCK	PA14	J2_59	PA14	PD9	J2_2	PD9	FMC_D14
	JTMS	PA13	J2_58	PA13	PD8	J2_3	PD8	FMC_D13
USART1_TX		PA9	J2_57	PA9	PE15	J2_4	PE15	FMC_D12
USART1_RX		PA10	J2_56	PA10	PE14	J2_5	PE14	FMC_D11
	USB_D-	PA11	J2_55	PA11	PE13	J2_6	PE13	FMC_D10
	USB_D+	PA12	J2_54	PA12	PE12	J2_7	PE12	FMC_D9
DCMI_XCLK	REMOTE_IN	PA8	J2_53	PA12	PE11	J2_8	PE11	FMC_D8
	NRF_CE	PG6	J2_52	PA8	PE10	J2_9	PE10	FMC_D7
DCMI_D0		PC6	J2_51	PG6	PE9	J2_10	PE9	FMC_D6
DCMI_D1		PC7	J2_50	PC6	PE8	J2_11	PE8	FMC_D5
	FMC_A18	PD13	J2_49	PC7	PE7	J2_12	PE7	FMC_D4
USART3_TX		PB10	J2_48	PD13	PE7	J2_13	PD1	FMC_D3
USART3_RX		PB11	J2_47	PB10	PD0	J2_14	PD0	FMC_D2
	SPI2_MOSI	PB15	J2_46	PB11	PD0	J2_15	PD15	FMC_D1
GBC_LED	NRF_IRQ	PB12	J2_45	PB15	PD15	J2_16	PD14	FMC_D0
	SPI2_MISO	PB14	J2_44	PB12	PD14	J2_17	PE5	SAII_SCKA
	SPI2_SCK	PB13	J2_43	PB14	PE5	J2_18	PE6	SAII_SDA
	GBC_KEY	PI11	J2_42	PB13	PE6	J2_19	PE4	SAII_FSA
	RMII_REF_CLK	PA1	J2_41	PI11	PE4	J2_20	PE3	SAII_SDB
	RMII CRS_DV	PA7	J2_40	PA1	PE3	J2_21	PC13	KEY2
USART2_TX	ETH_MDIO	PA2	J2_39	PA7	PC13	J2_22	PH2	KEY1
	RMII_RXD1	PC5	J2_38	PA2	PH2	J2_23	PH3	KEY0
	RMII_RXD0	PC4	J2_37	PC5	PH3	J2_24	PB0	LED1
	ETH_MDC	PC1	J2_36	PC4	PB0	J2_25	PB1	LED0
USART2_RX	PWM_DAC	PA3	J2_35	PC1	PB1	J2_26	PH5	IIC_SDA
	FMC_NE1	PD7	J2_34	PA3	PH5	J2_27	PH4	IIC_SCL
	FMC_NOE	PD4	J2_33	PD7	PH4	J2_28	PA5	STM_ADC
	FMC_NWE	PD5	J2_32	PD4	PA5	J2_29	PA4	STM_DAC
	VREF+	J2_31	VREF+	PD5	PA4	J2_30	PA6	DCMI_HREF
				PD5	PA6			DCMI_PCLK

VBAT

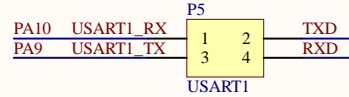


IO

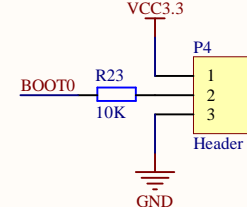
PH5	1	2	GND	GND	P3	1	2	PB1
PG10	3	4	PH4	PA0	3	4	PB0	
PB9	5	6	PA8	PH2	5	6	PH3	
PD3	7	8	PA6	PE3	7	8	PC13	
PC7	9	10	PB8	PE6	9	10	PE4	
PG12	11	12	PC6	PE2	11	12	PE5	
PB3	13	14	PB7	PB12	13	14	PI11	
PB4	15	16	PD5	PA1	15	16	PC1	
PD4	17	18	PD7	PA7	17	18	PC4	
PD13	19	20	PB13	PC5	19	20	PB5	
PB14	21	22	PB15	PG7	21	22	PI8	
PG6	23	24	PA13	PH6	23	24	PG3	
PA14	25	26	PA15	PG11	25	26	PH7	
PI15	27	28	PJ0	PG14	27	28	PG13	
PJ1	29	30	PJ2	PI13	29	30	PI12	
PJ3	31	32	PJ4	PK7	31	32	PI14	
PJ5	33	34	PJ6	PK5	33	34	PK6	
PJ7	35	36	PJ8	PK3	35	36	PK4	
PJ9	37	38	PJ10	PJ14	37	38	PJ15	
PC8	39	40	PC9	PJ12	39	40	PJ13	
PC11	41	42	PC12	PK1	41	42	PK2	
PC10	43	44	PD2	PJ11	43	44	PK0	

P7	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
PD14	PD15	PD0	PD1	PD7	PD8	PD9	PD10	PD11	PD12	PD13	PD14	PD15	PD8	PD9	PD10	

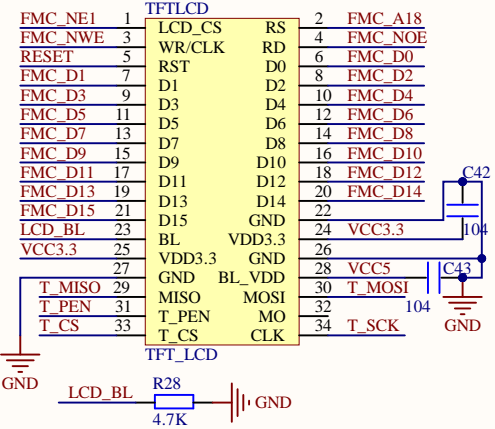
USB_UART/USART1



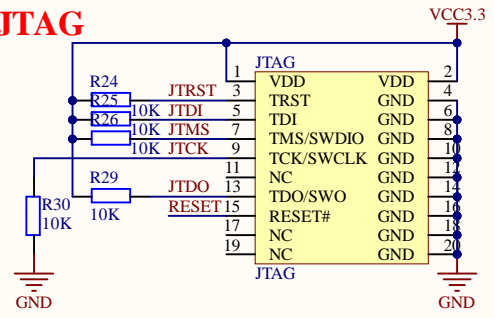
BOOT



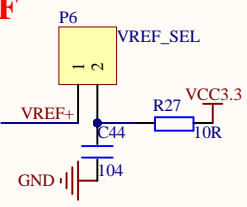
LCD(MCU)



JTAG



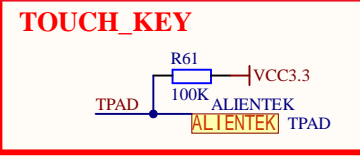
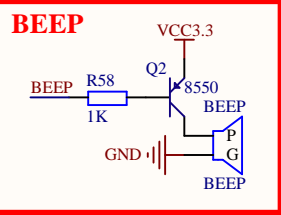
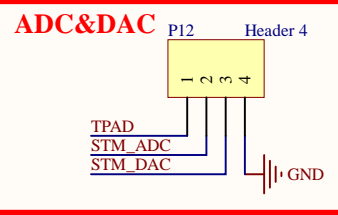
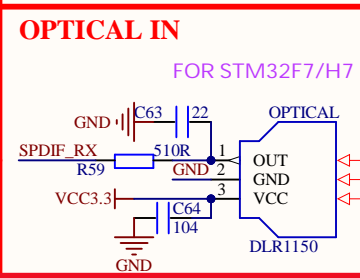
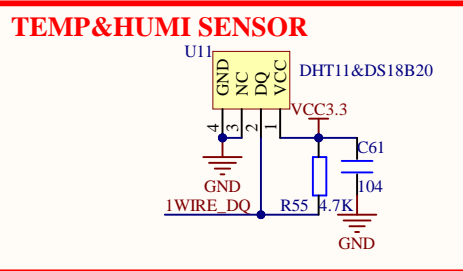
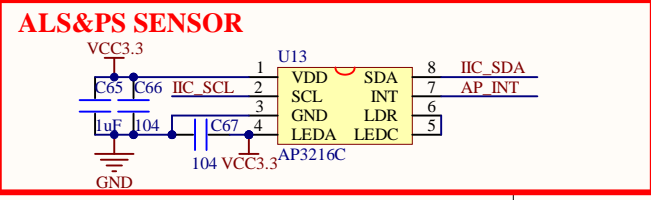
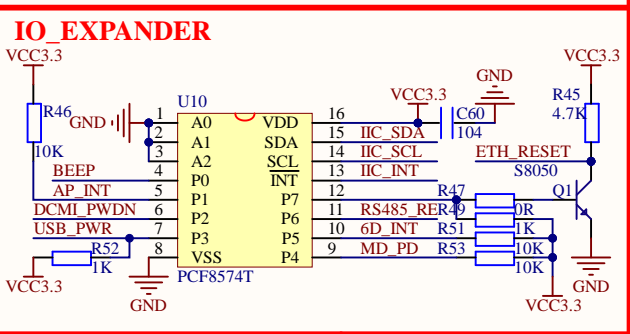
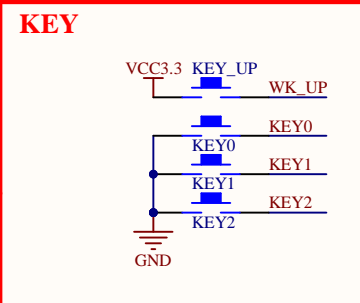
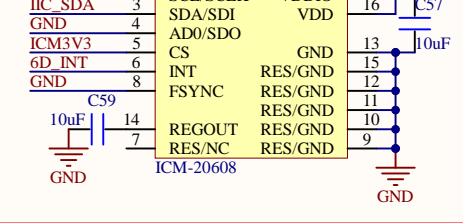
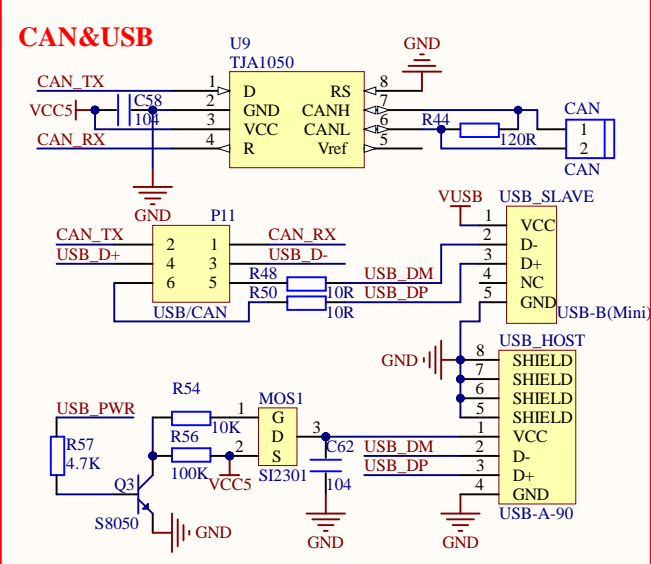
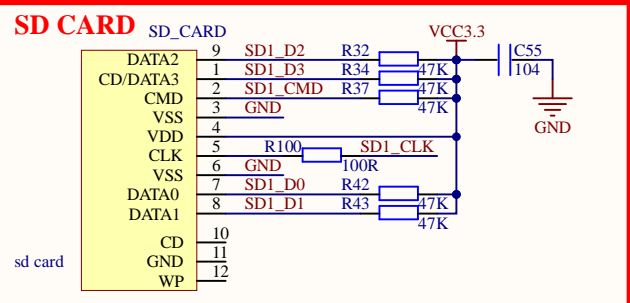
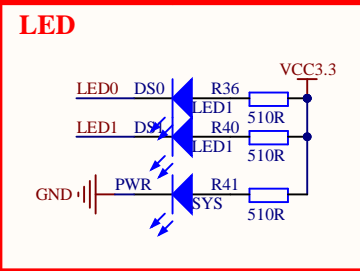
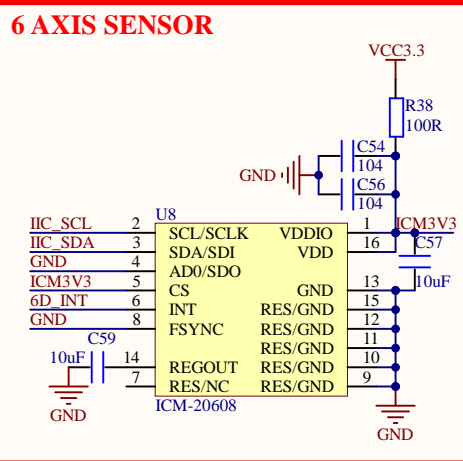
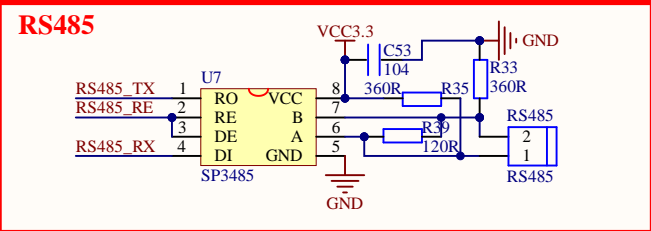
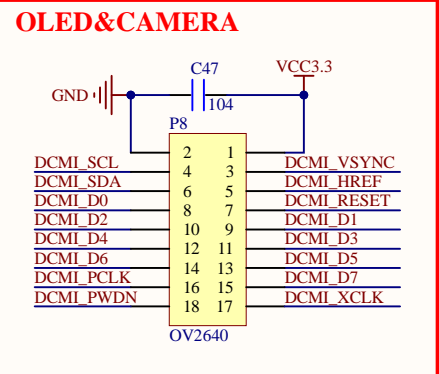
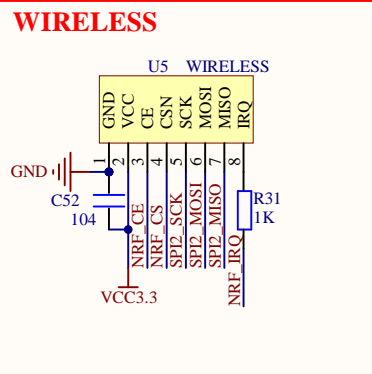
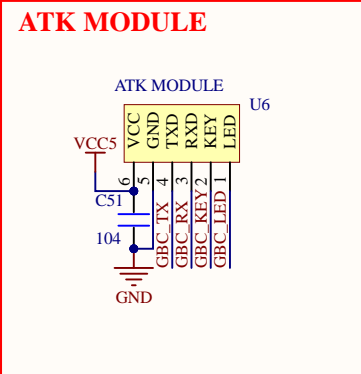
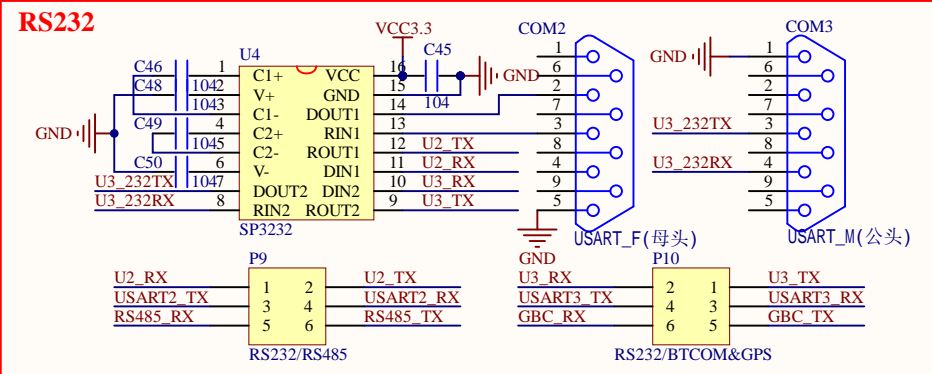
VREF



MCU参考电压选择端口，同时可以控制核心板上LED的亮灭，如果P5短接，则核心板上的电源灯(PWR)和DS0都不会亮。如果需要核心板上的LED亮，拔掉P5的跳线帽即可。

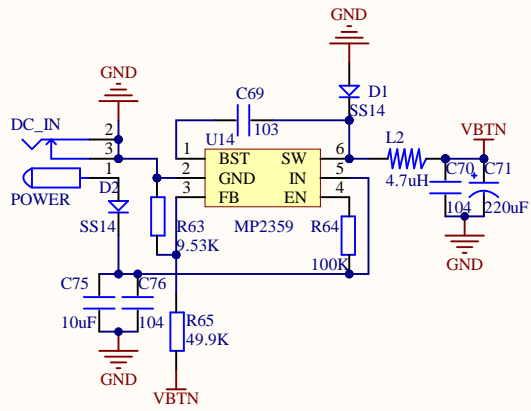
Title: CORE.SchDoc	
Project: STM32_MOTHERBOARD_V1.2.PjPcb	
Size: A4	Author: lycreturn@ALIENTEK
Date: 2021/11/12	Version: V1.0
Sheet: * of *	



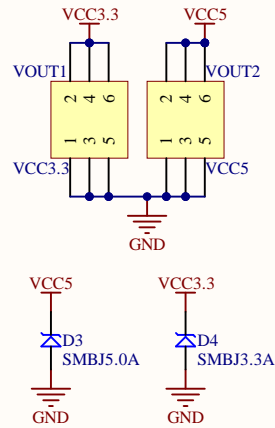


Title: DEVICE.SchDoc		
Project: STM32_MOTHERBOARD_V1.2.PjPcb		
Size: A4	Author: lycreturn@ALIENTEK	
Date: 2021/11/12	Version: V1.0	

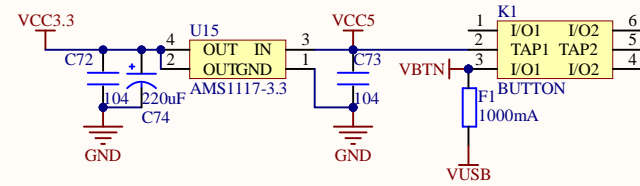
DC POWER IN



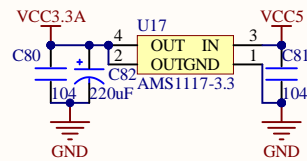
ON BOARD POWER SOURCE



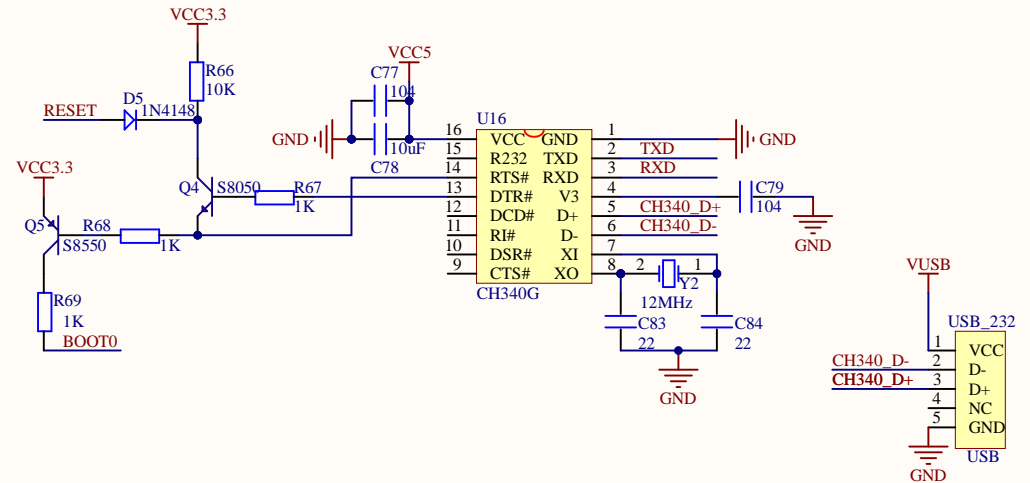
3.3V & POWER SWITCH



WM8978 ANALOG POWER



USB USART&USB POWER



Title: POWER&USB_USART.SchDoc	
Project: STM32_MOTHERBOARD_V1.2.PrjPcb	
Size: A4	Author: lycreturn@ALIENTEK
Date: 2021/11/12	Version: V1.0
Sheet: * of *	

