

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

## TD62101P,TD62101F,TD62103P,TD62103F TD62104P,TD62104F,TD62105P,TD62105F

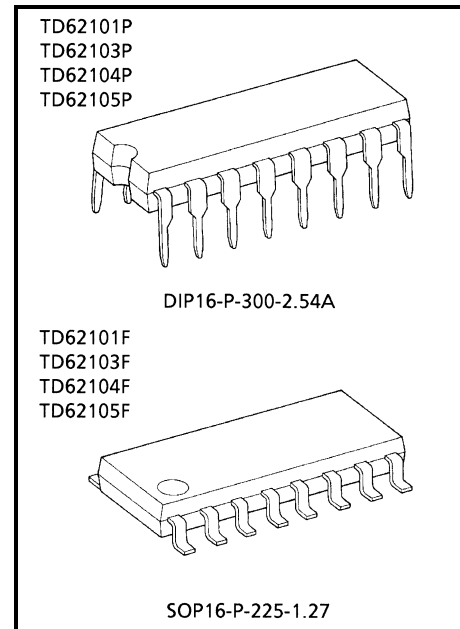
### 7CH DARLINGTON SINK DRIVER

The TD62101P / F series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs.

#### FEATURES

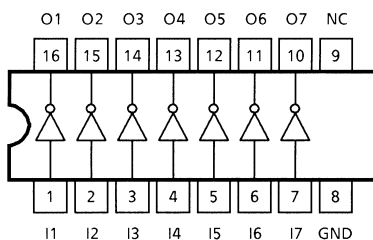
- Output current (single output) : 500 mA (max)
- High sustaining voltage output: 25 V (min)
- Inputs compatible with various types of logic.
- Package type-P : DIP-16 pin.
- Package type-F : SOP-16 pin.

TYPE	INPUT BASE RESISTOR	DESIGNATION
TD62101P / F	External	General Purpose
TD62103P / F	2.7kΩ	TTL, 5 V CMOS
TD62104P / F	10.5kΩ	6~15 V CMOS, PMOS
TD62105P / F	20kΩ	12~25 V CMOS, PMOS



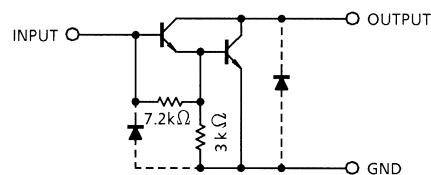
Weight  
 DIP16-P-300-2.54A: 1.11 g (typ.)  
 SOP16-P-225-1.27: 0.16 g (typ.)

#### PIN CONNECTION (TOP VIEW)



#### SCHEMATICS (EACH DRIVER)

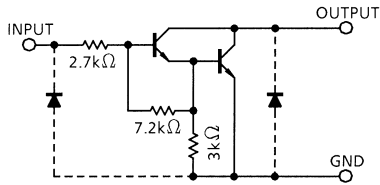
##### TD62101P / F



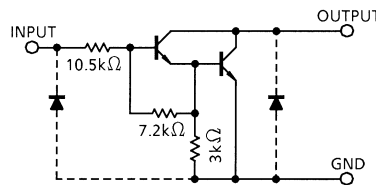
Note: The input and output parasitic diodes cannot be used as clamp diodes.

## SCHEMATICS (EACH DRIVER)

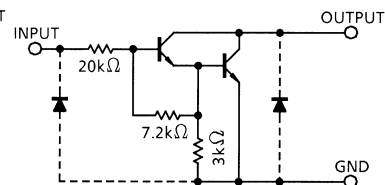
**TD62103P / F**



**TD62104P / F**



**TD62105P / F**



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTICS		SYMBOL	RATING	UNIT
Output Sustaining Voltage		$V_{CE(SUS)}$	-0.5~25	V
Output Current		$I_{OUT}$	500	mA / ch
Input Voltage		$V_{IN}$ (Note 1)	-0.5~30	V
Input Current		$I_{IN}$ (Note 2)	25	mA
Power Dissipation	P	$P_D$	1.0	W
	F		0.625 (Note 3)	
Operating Temperature	P	$T_{opr}$	-30~75	°C
	F		-40~85	
Storage Temperature		$T_{stg}$	-55~150	°C

Note 1: Except TD62101P / F

Note 2: Only TD62101P / F

Note 3: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

## RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C and Ta = -30~75°C for only Type-P)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Output Sustaining Voltage		$V_{CE(SUS)}$		0	—	25	V
Output Current		$I_{OUT}$	DC 1 Circuit	0	—	350	mA / ch
			$T_{pw} = 25$ ms, Duty = 10% 7 Circuits, Ta = 85°C, Tj = 120°C	0	—	300	
Input Voltage	Except TD62101P / F	$V_{IN}$		0	—	20	V
Input Current	Only TD62101P / F	$I_{IN}$		—	—	10	mA
Power Dissipation	P	$P_D$		—	—	0.44	W
	F		(Note)	—	—	0.325	

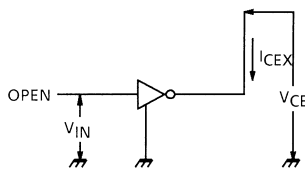
Note: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

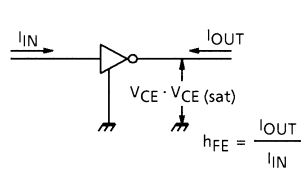
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT			
Output Leakage Current	P	$I_{CEX}$	1	$V_{CE} = 25\text{ V}$ $I_{IN} = 0$	$T_a = 75^\circ\text{C}$	—	—	100	$\mu\text{A}$		
	F				$T_a = 85^\circ\text{C}$	—	—	100			
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	2		$I_{OUT} = 350\text{ mA}, I_{IN} = 600\text{ }\mu\text{A}$	—	1.3	2.2	V		
					$I_{OUT} = 200\text{ mA}, I_{IN} = 400\text{ }\mu\text{A}$	—	1.1	2.0			
					$I_{OUT} = 100\text{ mA}, I_{IN} = 200\text{ }\mu\text{A}$	—	1.0	1.8			
DC Current Transfer Ratio		$h_{FE}$	2	$V_{CE} = 2\text{ V}, I_{OUT} = 350\text{ mA}$	1000	—	—				
Input Current	Output On	$I_{IN(ON)}$	3	$V_{IN} = 1.5\text{ V}, I_{OUT} = 350\text{ mA}$	TD62101P / F	—	0.25	—	mA		
					TD62103P / F	—	1.00	—			
					TD62104P / F	—	0.4	0.7			
					TD62105P / F	—	1.2	1.7			
					TD62105P / F	—	1.0	1.5			
	Output Off	$I_{IN(OFF)}$	4		$I_{OUT} = 500\text{ }\mu\text{A}$	$T_a = 75^\circ\text{C}$	50	65	—	$\mu\text{A}$	
						$T_a = 85^\circ\text{C}$	50	65	—		
						TD62103P / F	—	—	2.1		V
						TD62104P / F	—	—	4		
						TD62105P / F	—	—	6.4		
TD62103P / F	—	—	2.7								
TD62104P / F	—	—	7								
TD62105P / F	—	—	12								
TD62103P / F	—	—	3.3								
TD62104P / F	—	—	8.8								
TD62105P / F	—	—	15								
Input Voltage		$V_{IN(ON)}$	5	$V_{CE} = 2\text{ V}$	$I_{OUT} = 125\text{ mA}$	—	—	2.1	V		
					$I_{OUT} = 250\text{ mA}$	—	—	4			
					$I_{OUT} = 350\text{ mA}$	—	—	6.4			
						—	—	2.7			
						—	—	7			
						—	—	12			
						—	—	3.3			
						—	—	8.8			
						—	—	15			
Input Capacitance		$C_{IN}$	6	$V_{IN} = 0, f = 1\text{ MHz}$	—	15	—	pF			
Turn-On Delay		$t_{ON}$	7	$V_{OUT} = 25\text{ V}, R_L = 70\text{ }\Omega$ $C_L = 15\text{ pF}$	—	0.1	—	$\mu\text{s}$			
Turn-Off Delay		$t_{OFF}$			—	0.2	—				

## TEST CIRCUIT

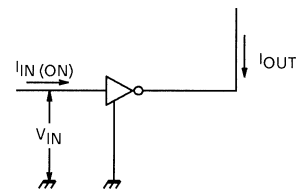
### 1. $I_{CEX}$



### 2. $h_{FE}, V_{CE(sat)}$

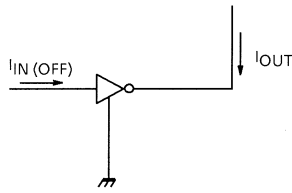


### 3. $I_{IN(ON)}$

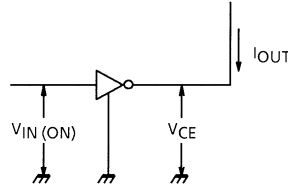


## TEST CIRCUIT

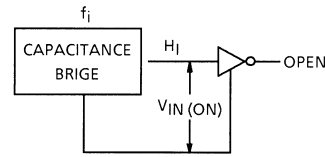
### 4. $I_{IN}$ (OFF)



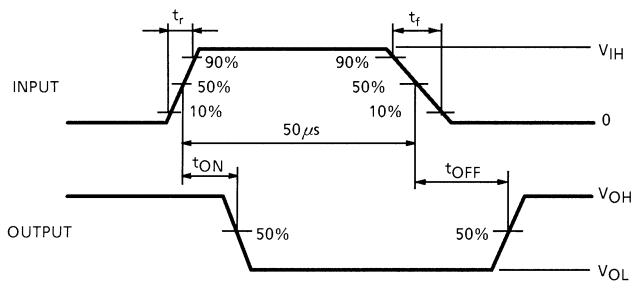
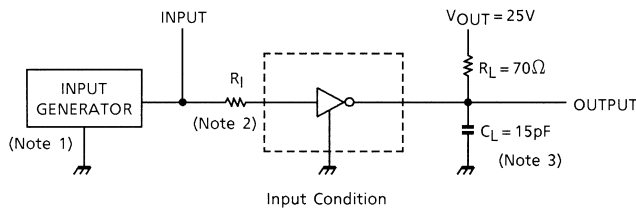
### 5. $V_{IN}$ (ON)



### 6. $C_{IN}$



### 7. $t_{ON}$ , $t_{OFF}$



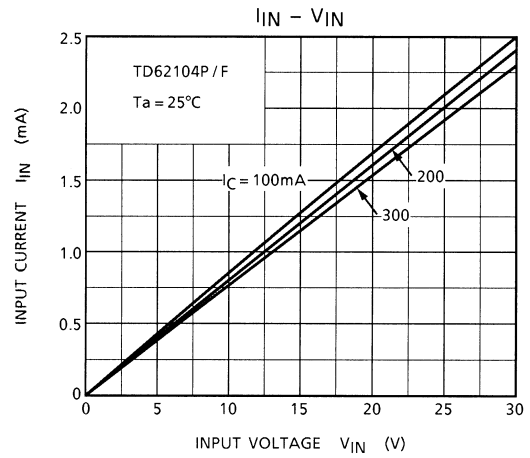
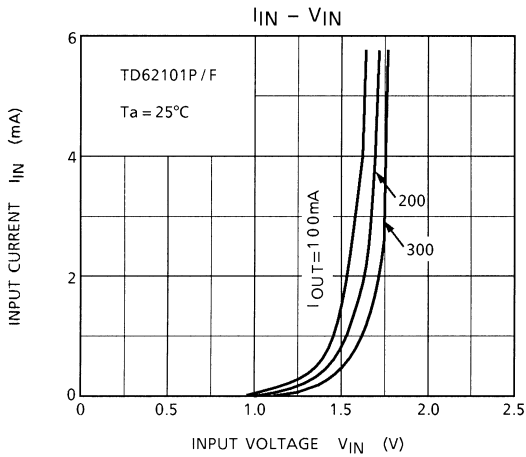
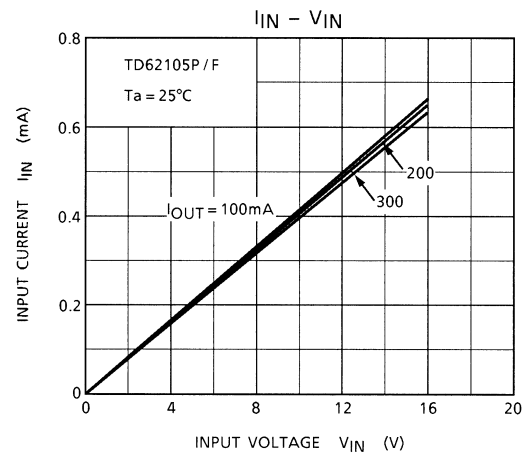
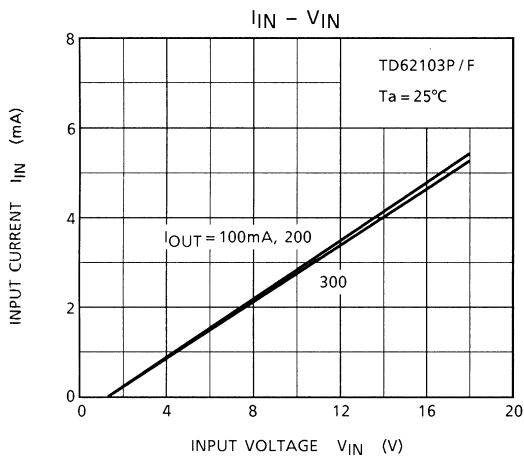
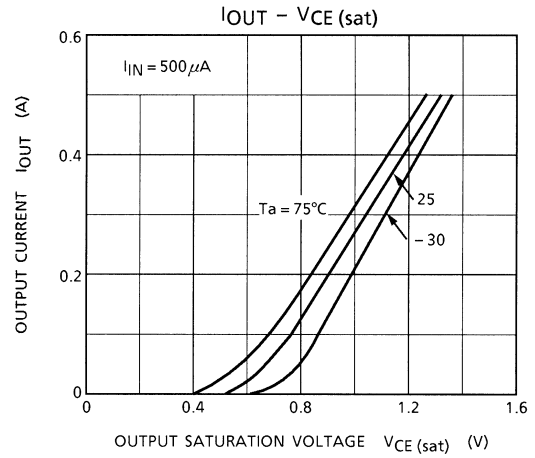
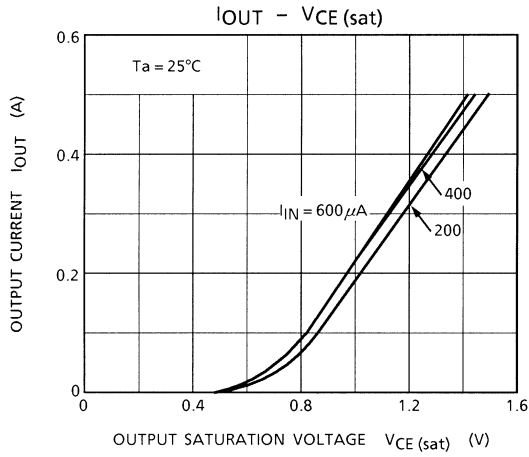
- Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10%  
Output Impedance 50  $\Omega$ ,  $t_r \leq 5$  ns,  $t_f \leq 10$  ns
- Note 2: See right.
- Note 3:  $C_L$  includes probe and jig capacitance.

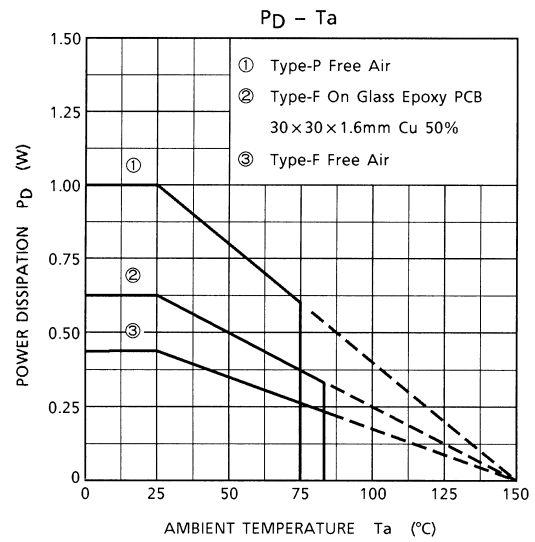
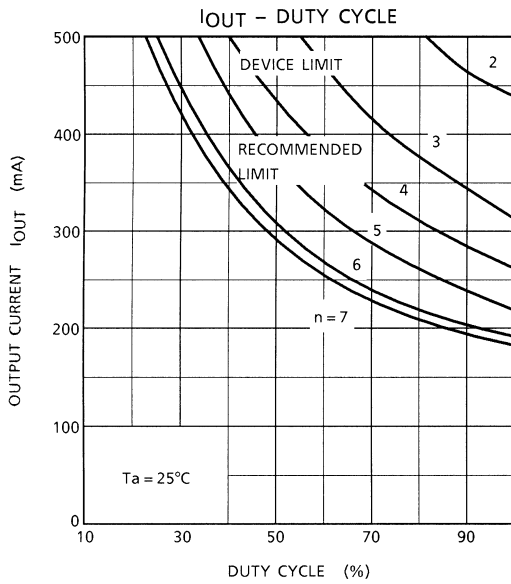
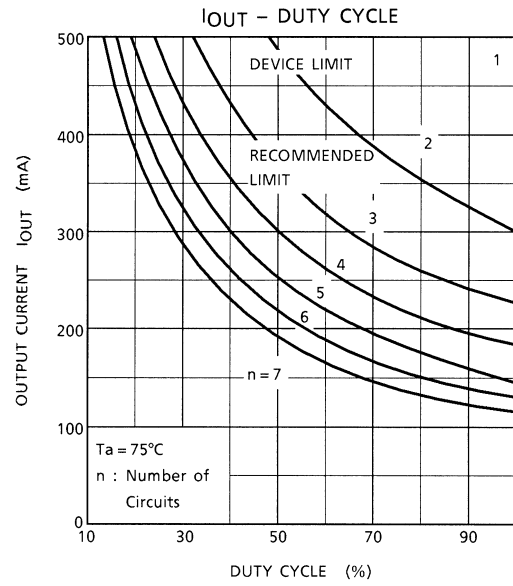
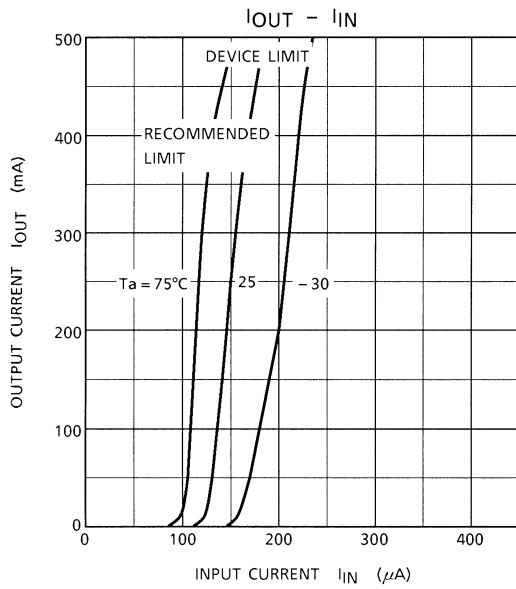
### INPUT CONDITION

TYPE NUMBER	$R_I$	$V_{IH}$
TD62101P / F	2.7 k $\Omega$	3 V
TD62103P / F	0 $\Omega$	3 V
TD62104P / F	0 $\Omega$	8 V
TD62105P / F	0 $\Omega$	15 V

### PRECAUTIONS for USING

This IC does not include built-in protection circuits for excess current or overvoltage. If this IC is subjected to excess current or overvoltage, it may be destroyed. Hence, the utmost care must be taken when systems which incorporate this IC are designed. Utmost care is necessary in the design of the output line, GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

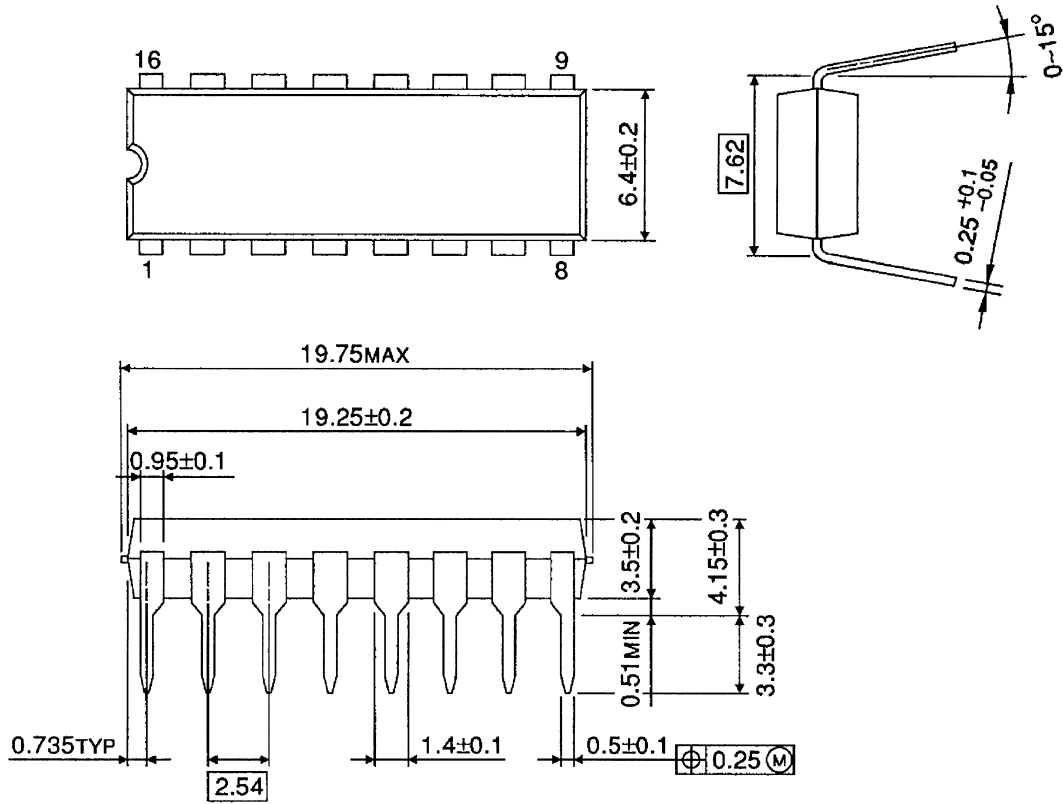




## PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit: mm

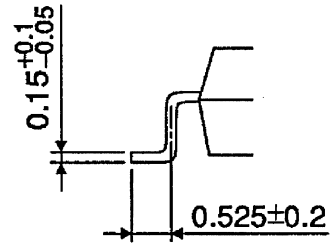
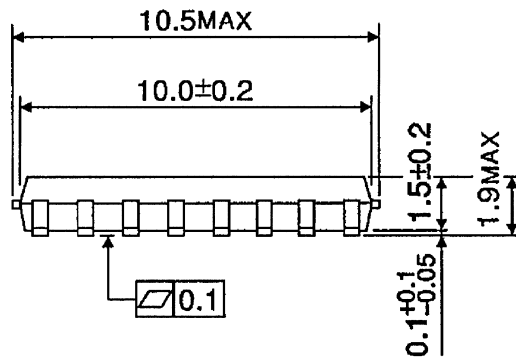
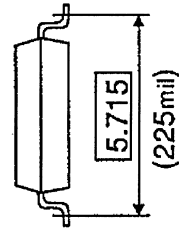
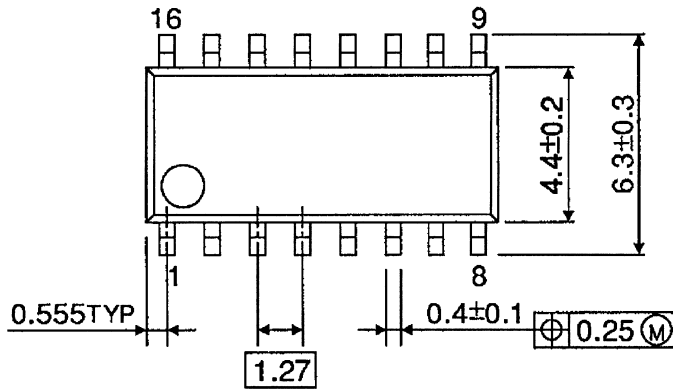


Weight: 1.11 g (typ.)

## PACKAGE DIMENSIONS

SOP16-P-225-1.27

Unit: mm



Weight: 0.16 g (typ.)



## Notes on Contents

### 1. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

### 2. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

## IC Usage Considerations

### Notes on Handling of ICs

- (1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.  
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- (2) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- (3) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.  
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- (4) Do not insert devices in the wrong orientation or incorrectly.  
Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.  
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.
- (5) Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator.  
If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

**Points to Remember on Handling of ICs**

## (1) Heat Radiation Design

In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature ( $T_j$ ) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into consideration the effect of IC heat radiation with peripheral components.

## (2) Back-EMF

When a motor rotates in the reverse direction, stops or slows down abruptly, a current flow back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

**RESTRICTIONS ON PRODUCT USE**

060116EBA

- The information contained herein is subject to change without notice. 021023\_D
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.  
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc. 021023\_A
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk. 021023\_B
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations. 060106\_Q
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others. 021023\_C
- The products described in this document are subject to the foreign exchange and foreign trade laws. 021023\_E