XDS100V2 USB JTAG Emulator for TI TMS320 and ARM Processors

Buy

T6033100

The XDS100v2 USB JTAG Emulator is the second release of the XDS100 JTAG emulator technology supporting debug of a variety of TI devices. The XDS100V JTAG connects to the PC via USB and to the microcontroller target board via the standard TI 14-pin (2x7) JTAG header. It is compatible with Code Composer Studio (CCS) V4 IDE from Texas Instruments and compatible with Windows 2000, XP, Vista (32-bit), and Win7 (32-bit).

XDS100V2 USB JTAG Emulator Features

- Support for the following processor cores: TMS320C28x, TMS320C54x, TMS320C55x, TMS320C64x+, TMS320C674x, ARM9, ARM Cortex-R4, and ARM Cortex-A8
- Support targets with 14-pin TI JTAG connector as used by Texas Instruments embedded processors
- Powered via USB
- Support for USB2.0 High Speed (480 Mbit/s)
- Compatible with +1.8V or +3.3V JTAG interfaces
- Support for Code Composer Studio v4 and newer (does **not** support Code Composer Studio v3.3)
- Supports Code Composer Studio C2000 On-Chip Flash Programmer
- Compatible with Windows 2000, XP, Vista (32-bit) and Win7 (32-bit)

XDS100v1 and XDS100v2 Comparison

There are 2 versions of the XDS100: 1) XDS100v1 and 2) XDS100v2. The XDS100v1 is TI's original XDS100 design. The two versions have some features in common but the XDS100v2 is an updated XDS100 design with more capabilities.

Features	XDS100v1	XDS100v2
1.8v and 3.3v IOs	Yes	Yes
JTAG reset/wait-in-reset boot-modes (using EMU0/1 + nTRST)	Yes	Yes
Power-on-Reset boot-modes (using EMU0/1 + TVD)	Yes	Yes
Target power-loss detection (using TVD)	Yes	Yes
Supports CCStudio v3.3	Yes	No
Supports CCStudio v4	No	Yes
USB 1.1 Full Speed (12 Mbit/s)	Yes	No
USB 2.0 High Speed (480 Mbit/s)	No	Yes
14-pin JTAG header	Yes	Yes
Processor Families Supported	TMS320C28xx TMS320C54xx TMS320C55xx TMS320C64x+ TMS320C674x	TMS320C28xx TMS320C54xx TMS320C55xx TMS320C64x+ TMS320C674x ARM 9 ARM Cortex-R4 ARM Cortex-A8

Ships from: China
Lead time: **4 weeks**