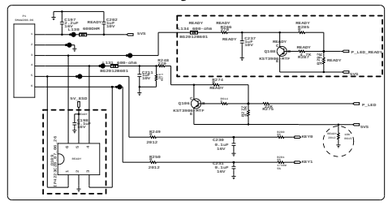
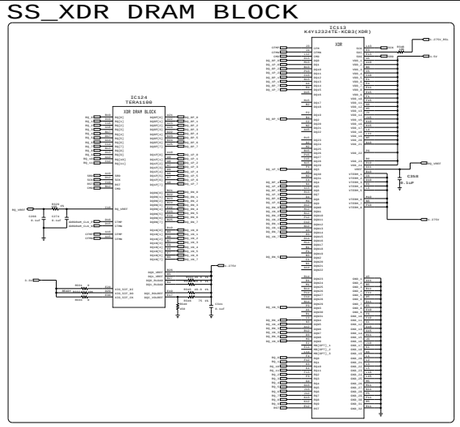
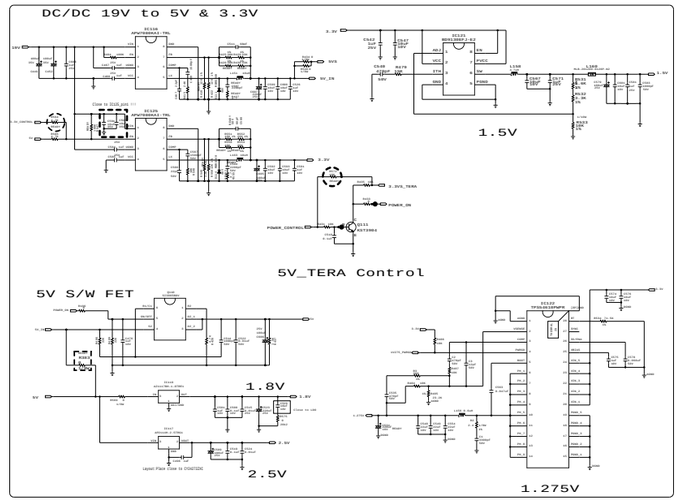
Control Key



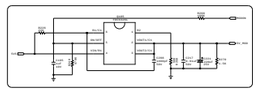
SS_XDR DRAM BLOCK



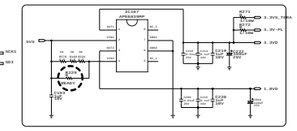
Power rail



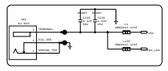
PANEL FET



5VS TO 3.3/1.8V

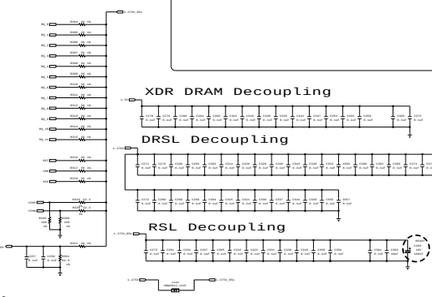


DC_IN Jack

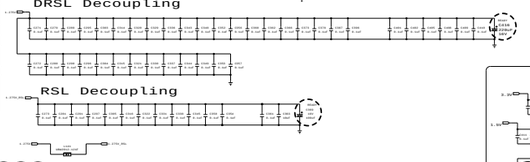


Pivot

XDR DRAM Decoupling

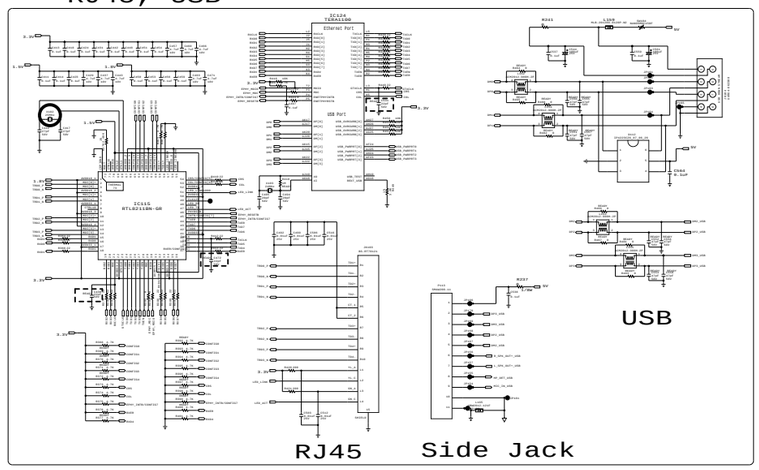


DRSL Decoupling

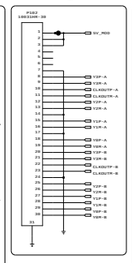


RSL Decoupling

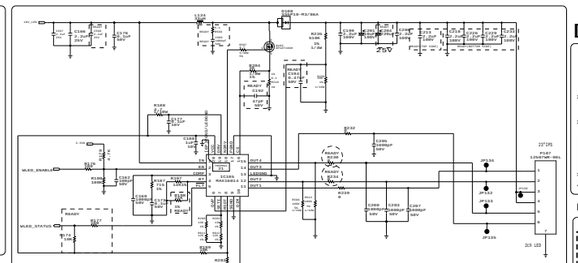
RJ45, USB



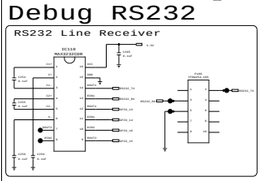
LVDS



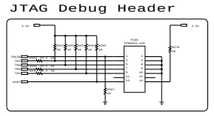
LED Driver_19"L



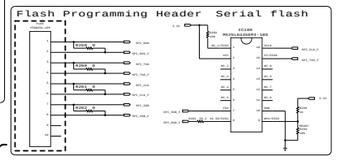
Debug RS232



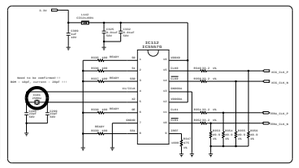
JTAG Debug Header



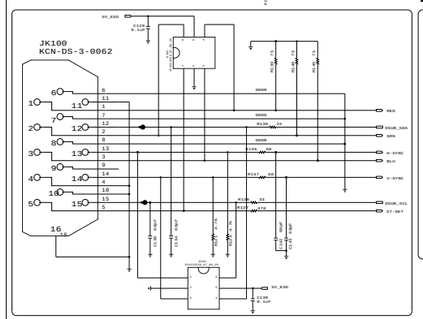
Boot Flash



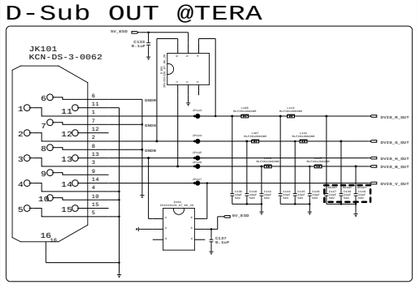
CLOCK IC



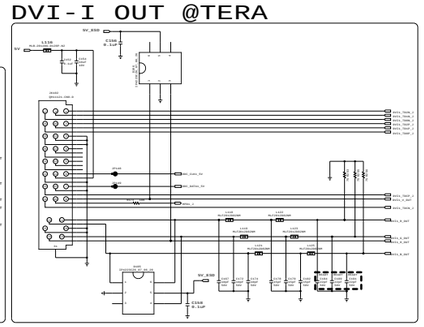
JK100 KCN-DS-3-0062



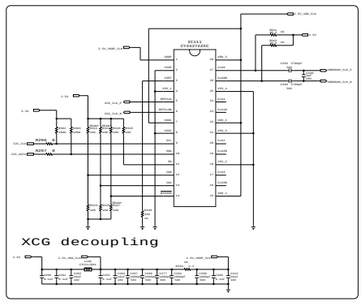
D-Sub OUT @TERA



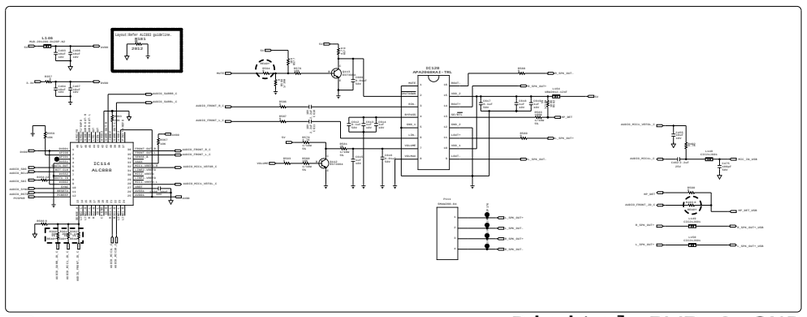
DVI-I OUT @TERA



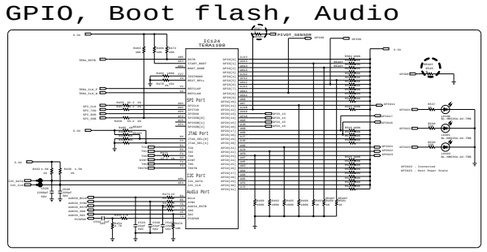
CLOCK Generator



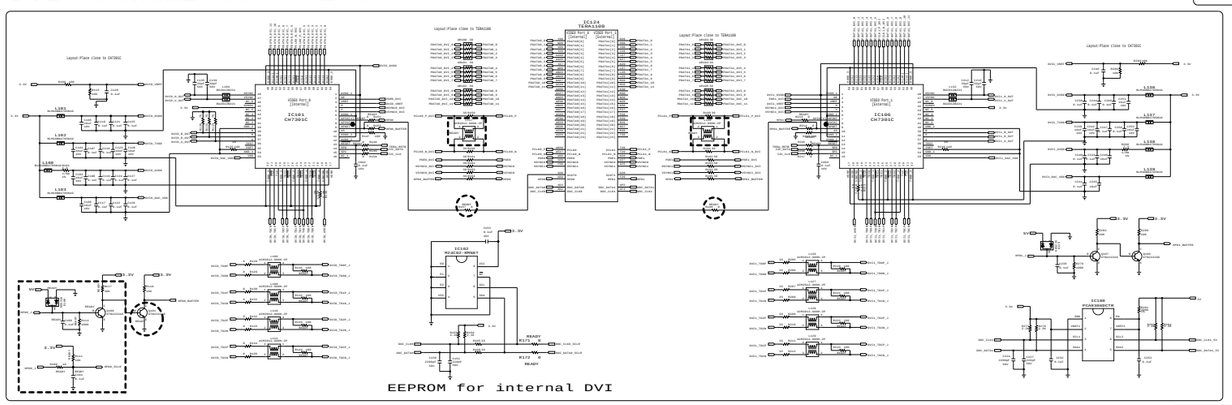
Audio



GPIO, Boot flash, Audio

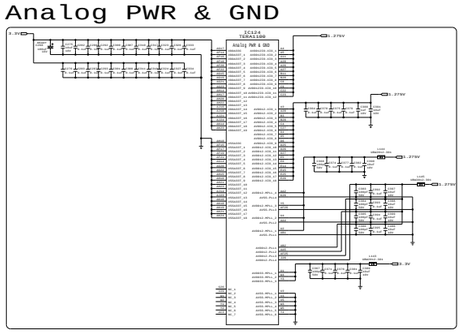


DVI Transmitter * 2ea

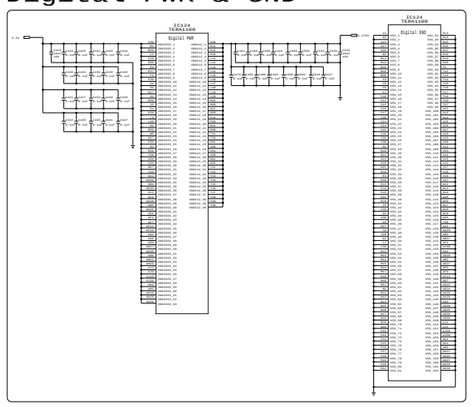


EEPROM for internal DVI

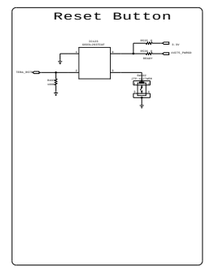
Analog PWR & GND



Digital PWR & GND



Button



SECRET
LGElectronics

LG ELECTRONICS

THE ⚠ SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS. WHEN SERVICING IT IS ESSENTIAL THAT ONLY MANUFATURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMATIC.

MODEL	N2311AZ	DATE	2011.09.15
BLOCK	MAIN	SHEET	1 / 1