

FGA180N33ATD

330 V PDP Trench IGBT

Features

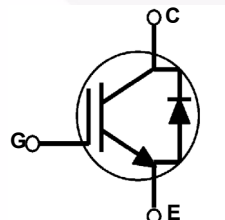
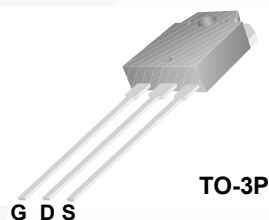
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.68 \text{ V} @ I_C = 180 \text{ A}$
- High Input Impedance
- RoHS Compliant

Applications

- PDP TV

General Description

Using novel trench IGBT Technology, Fairchild's new series of trench IGBTs offer the optimum performance for PDP TV applications where low conduction and switching losses are essential.



Absolute Maximum Ratings

Symbol	Description	Ratings	Unit
V_{CES}	Collector to Emitter Voltage	330	V
V_{GES}	Gate to Emitter Voltage	± 30	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	180	A
$I_{CM(1)}$	Pulsed Collector Current @ $T_C = 25^\circ\text{C}$	450	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	390	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	156	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Notes:

1: Repetitive test, pulse width = 100usec, Duty = 0.1

* I_{C_pulse} limited by max T_J

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction to Case	-	0.32	$^\circ\text{C/W}$
$R_{\theta JC}(\text{Diode})$	Thermal Resistance, Junction to Case	-	0.82	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	40	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGA180N33ATD	FGA180N33ATDTU	TO-3P	-	-	30ea

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 400\mu A$	330	-	-	V
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	400	μA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	± 400	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 250\mu A, V_{CE} = V_{GE}$	2.5	4.0	5.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 40A, V_{GE} = 15V$	-	1.1	1.4	V
		$I_C = 180A, V_{GE} = 15V,$	-	1.68	-	V
		$I_C = 180A, V_{GE} = 15V$ $T_C = 125^\circ C$	-	1.89	-	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$ $f = 1MHz$	-	3880	-	pF
C_{oes}	Output Capacitance		-	305	-	pF
C_{res}	Reverse Transfer Capacitance		-	180	-	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 200V, I_C = 40A,$ $R_G = 5\Omega, V_{GE} = 15V,$ Resistive Load, $T_C = 25^\circ C$	-	27	-	ns
t_r	Rise Time		-	80	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	108	-	ns
t_f	Fall Time		-	180	240	ns
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 200V, I_C = 40A,$ $R_G = 5\Omega, V_{GE} = 15V,$ Resistive Load, $T_C = 125^\circ C$	-	26	-	ns
t_r	Rise Time		-	75	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	112	-	ns
t_f	Fall Time		-	250	300	ns
Q_g	Total Gate Charge	$V_{CE} = 200V, I_C = 40A,$ $V_{GE} = 15V$	-	169	-	nC
Q_{ge}	Gate to Emitter Charge		-	22	-	nC
Q_{gc}	Gate to Collector Charge		-	69	-	nC

Electrical Characteristics of the Diode T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max	Unit	
V _{FM}	Diode Forward Voltage	I _F = 20A	T _C = 25°C	-	1.2	1.6	V
			T _C = 125°C	-	1.04	-	
t _{rr}	Diode Reverse Recovery Time	I _F = 20A, di _F /dt = 200A/μs	T _C = 25°C	-	27	-	ns
			T _C = 125°C	-	39	-	
I _{rr}	Diode Peak Reverse Recovery Current	I _F = 20A, di _F /dt = 200A/μs	T _C = 25°C	-	3.5	-	A
			T _C = 125°C	-	6.0	-	
Q _{rr}	Diode Reverse Recovery Charge	I _F = 20A, di _F /dt = 200A/μs	T _C = 25°C	-	48	-	nC
			T _C = 125°C	-	117	-	



Typical Performance Characteristics

Figure 1. Typical Output Characteristics

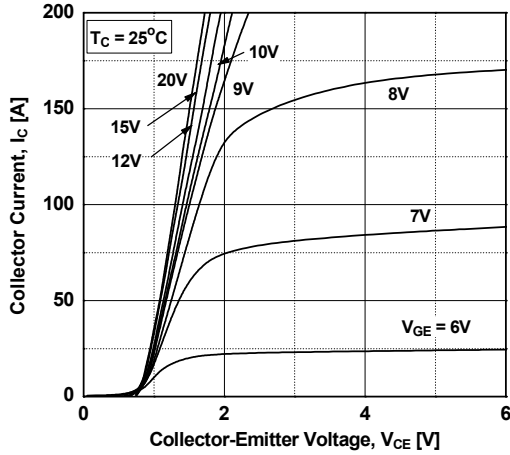


Figure 2. Typical Output Characteristics

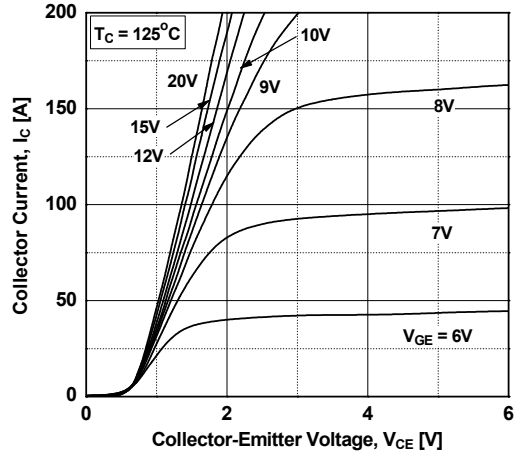


Figure 3. Typical Saturation Voltage Characteristics

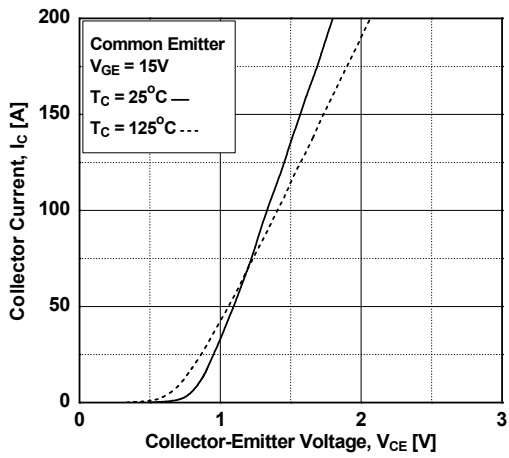


Figure 4. Transfer Characteristics

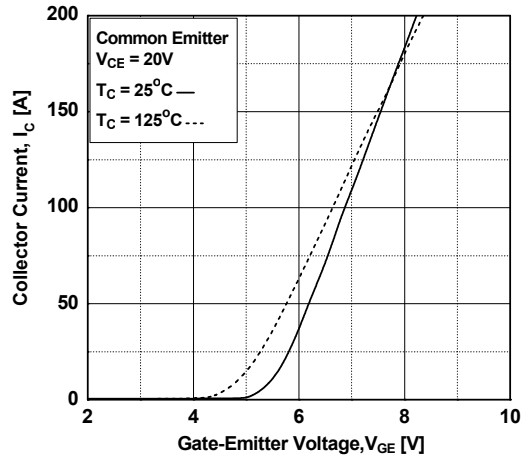


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

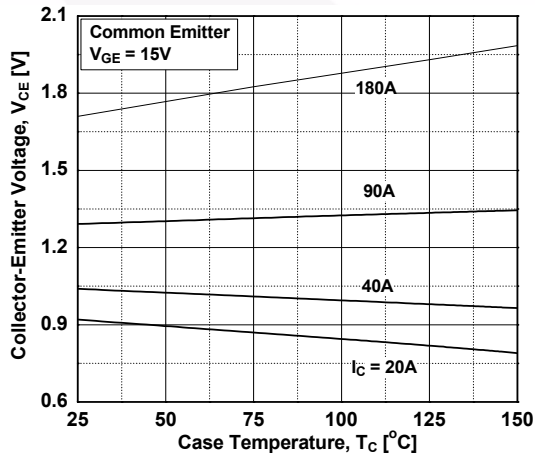
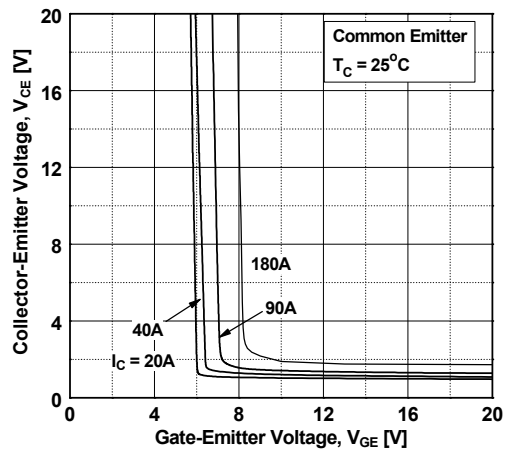


Figure 6. Saturation Voltage vs. Vge



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

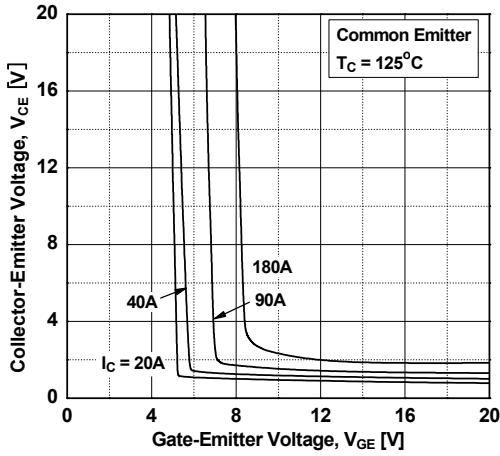


Figure 8. Capacitance Characteristics

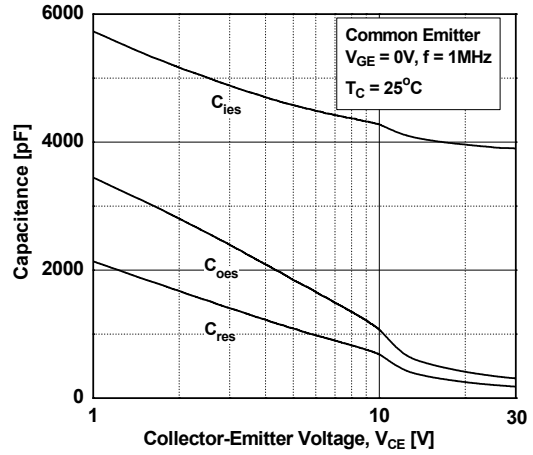


Figure 9. Gate charge Characteristics

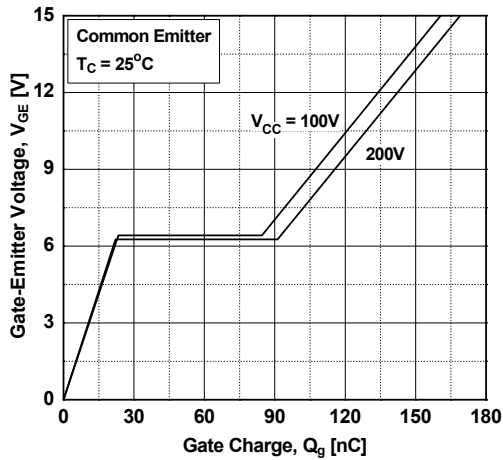


Figure 10. SOA Characteristics

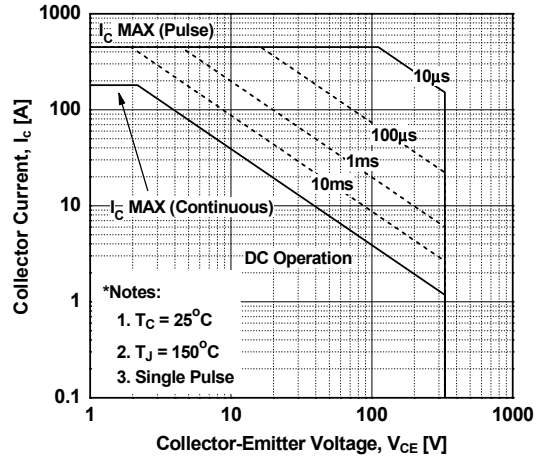


Figure 11. Turn-on Characteristics vs. Gate Resistance

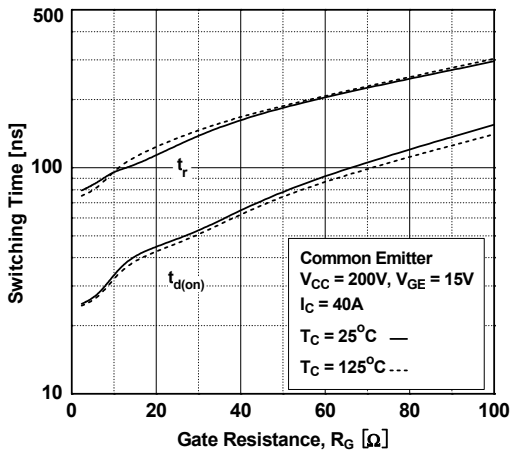
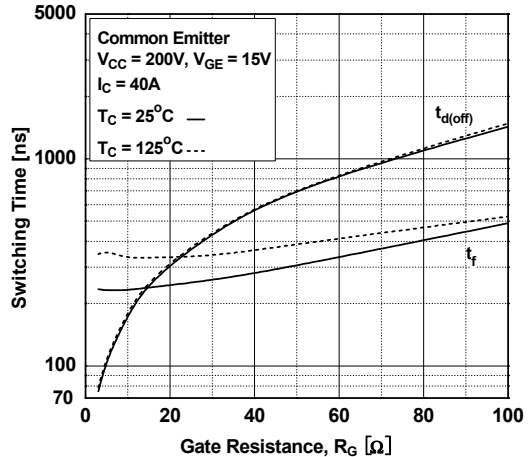


Figure 12. Turn-off Characteristics vs. Gate Resistance



Typical Performance Characteristics

Figure 13. Turn-on Characteristics vs. Collector Current

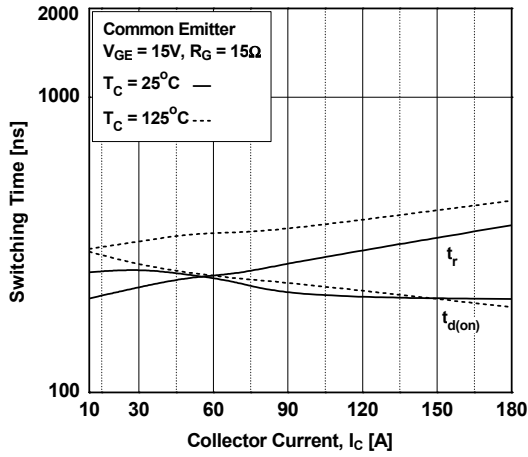


Figure 14. Turn-off Characteristics vs. Collector Current

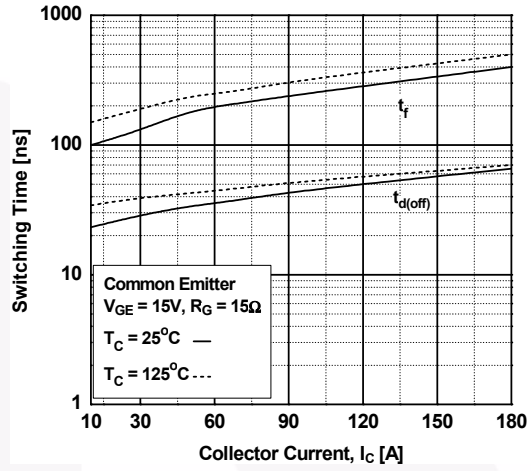


Figure 15. Turn off Switching SOA Characteristics

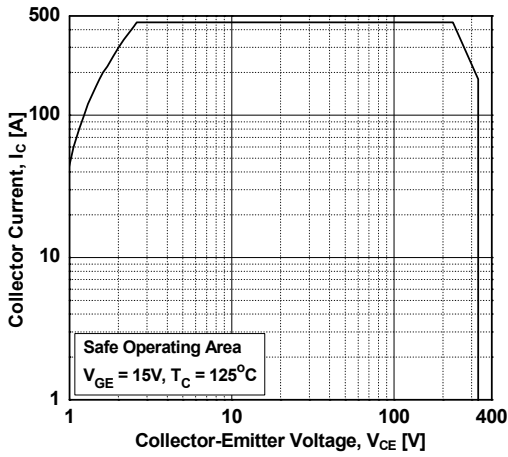
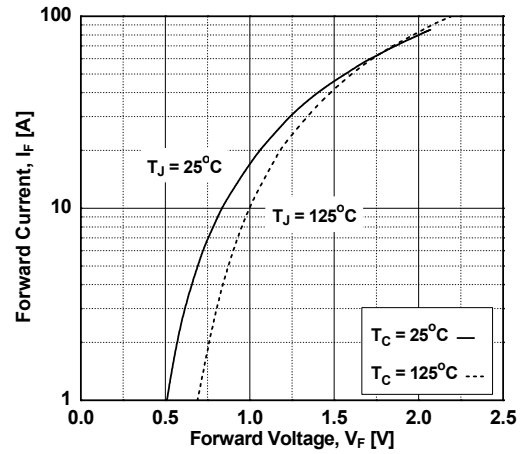


Figure 16. Forward Characteristics



Typical Performance Characteristics

Figure 17. Reverse Recovery Current

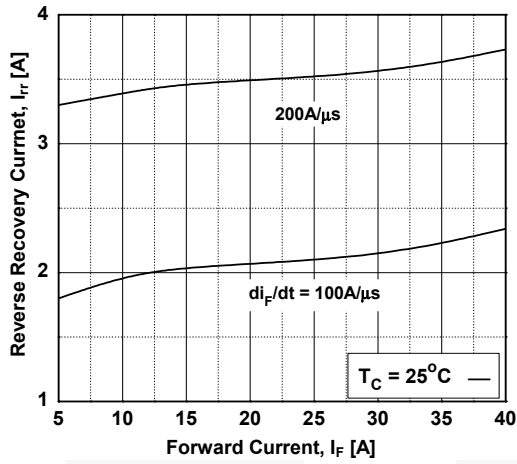


Figure 18. Stored Charge

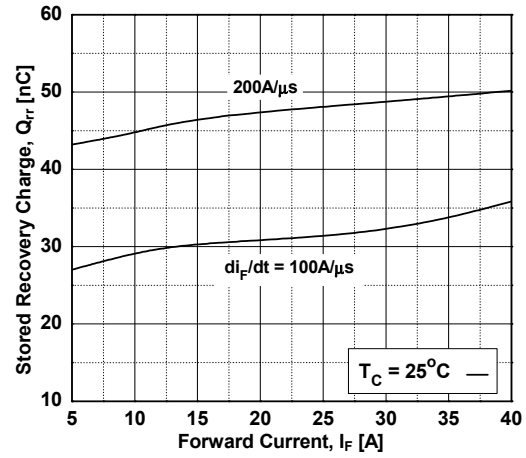


Figure 19. Reverse Recovery Time

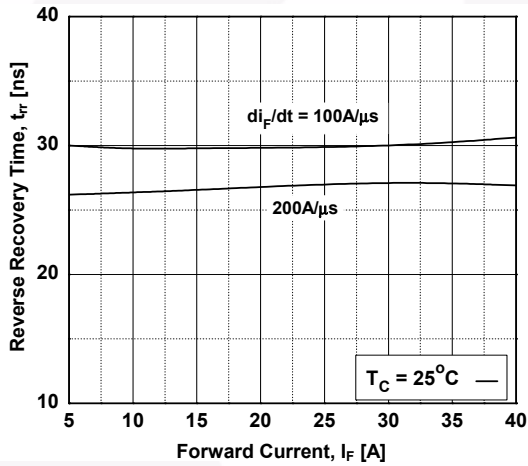
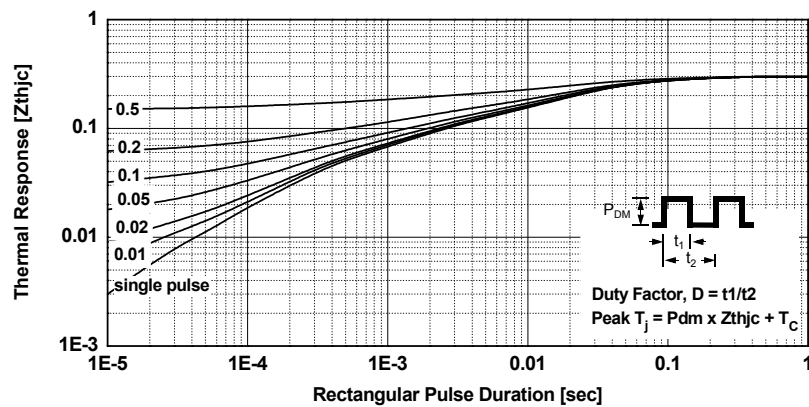
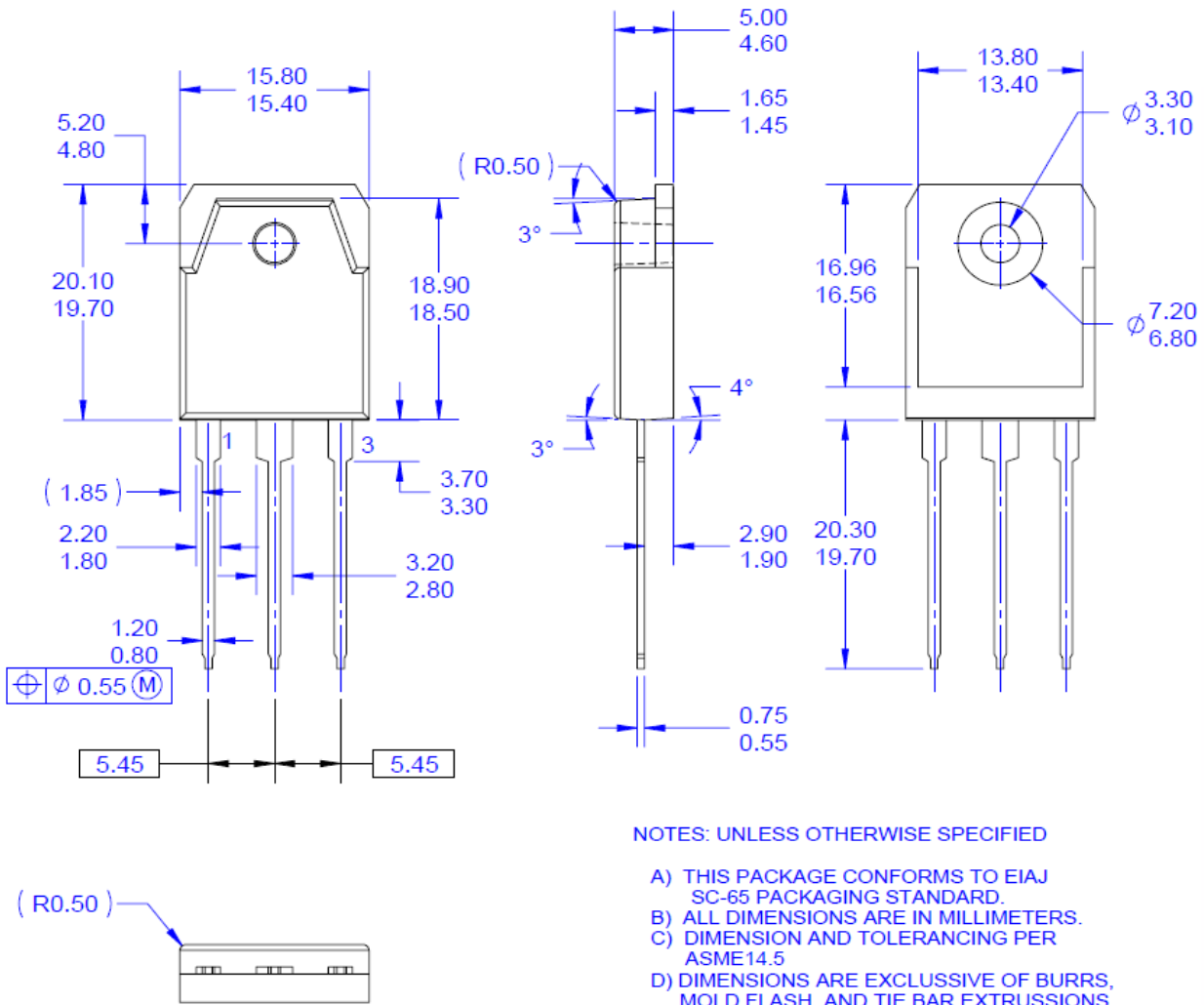


Figure 20. Transient Thermal Impedance of IGBT



Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSION AND TOLERANCING PER ASME14.5
- D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- E) THIS PACKAGE IS INTENDED ONLY FOR T03PN.
- F) DRAWING FILE NAME: T03P03AREV4.

Figure 21. TO-3P 3L - 3LD, T03, PLASTIC, EIAJ SC-65

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:



http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TT3P0-003

Dimensions in Millimeters



TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

- | | | | |
|--|-------------------------------------|---|---|
| AccuPower™ | F-PFS™ | PowerTrench® | Sync-Lock™ |
| AX-CAP®* | FRFET® | PowerXS™ | SYSTEM GENERAL®* |
| BitSiC™ | Global Power Resource SM | Programmable Active Droop™ | TinyBoost® |
| Build it Now™ | GreenBridge™ | QFET® | TinyBuck® |
| CorePLUS™ | Green FPS™ | QS™ | TinyCalc™ |
| CorePOWER™ | Green FPS™ e-Series™ | Quiet Series™ | TinyLogic® |
| CROSSVOLT™ | Gmax™ | RapidConfigure™ | TINYOPTO™ |
| CTL™ | GTO™ | ISOPLANAR™ | TinyPower™ |
| Current Transfer Logic™ | IntelliMAX™ | Marking Small Speakers Sound Louder and Better™ | TinyPWM™ |
| DEUXPEED® | ISOPLANAR™ | MegaBuck™ | TinyWire™ |
| Dual Cool™ | MICROCOUPLER™ | MicroFET™ | TranSiC™ |
| EcoSPARK® | MicroPak™ | MillerDrive™ | TriFault Detect™ |
| EfficientMax™ | MicroPak2™ | MotionMax™ | TRUECURRENT®* |
| ESBC™ | mWSaver® | OPTOLOGIC® | μSerDes™ |
|  Fairchild® | OptoHit™ | OPTOPLANAR® |  SerDes™ |
| Fairchild Semiconductor® | SPM® | STEALTH™ | UHC® |
| FACT Quiet Series™ | SupreMOS® | SuperFET® | Ultra FRFET™ |
| FACT® | SyncFET™ | SuperSOT™-3 | UniFET™ |
| FAST® | | SuperSOT™-6 | VCX™ |
| FastvCore™ | | SuperSOT™-8 | VisualMax™ |
| FETBench™ | | | VoltagePlus™ |
| FPS™ | | | XS™ |

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I66

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Fairchild Semiconductor:](#)

[FGA180N33ATDTU](#)