

## LSI® 12Gb/s SAS/SATA Expander

LSI SAS3x36R, LSI SAS3x28R, LSI SAS3x24R



### Applications

- Server backplanes
- SAS/SATA drive enclosures
- External RAID subsystems
- JBOD architectures

### Features

- Integrated Enclosure Processor
  - ARM® Cortex-R4 @ 300MHz
- 12Gb/s SAS Interface
  - 36, 28, 24-port SAS/SATA expanders
  - Supports 12Gb/s, 6Gb/s, and 3Gb/s SAS, and 6Gb/s and 3Gb/s SATA data transfer rates
  - Ports are non-denominational - initiator and target connect
  - Supports narrow and wide port configurations
  - Spread spectrum clocking
  - Supports SSP initiator and target, SMP Initiator and target, STP initiator and SATA protocols
  - DataBolt Bandwidth Optimizer for SATA end devices
- T-10 zoning – 255 zones
- Port mirroring
- 16 GPIO Pins (64)
- 1 LED Pins per Phy
  - Configurable for link status, drive activity, fault LED outputs
  - Can be used as GPIOs
- SFF-8485 Compliant (2 SGPIO ports)
- External memory interface support
  - 8/16 bit FLASH/NVSRAM
- Communication Interfaces
  - Five I2C interfaces for enclosure management services or debug
  - Two UART interfaces for debug as well as an out of band communication port
- JTAG support
- 672 FPB32GU (27mm x 27mm)

Optimized footprint for server backplanes with limited space, LSI SAS expanders offer scalability, fault tolerance, and storage management support

### Overview

The 12Gb/s SAS Expanders are LSI's seventh generation of expanders that incorporate the latest enhancements in SAS technology. The 36, 28, 24-port option expanders are space optimized for server backplane and other implementations requiring a small footprint solution for storage expansion. The 12Gb/s SAS/SATA expanders are designed with an integrated ARM Cortex-R4 processor for topology management functions such as discovery, enclosure and drive management, and LED management. The expanders support SAS data transfer rates of 12, 6, 3 Gb/s, and SATA data transfer rates of 6 and 3Gb/s.

The LSI SAS expanders also incorporate the latest enhancements in SAS technology which includes Spread Spectrum Clocking for EMI reduction and T-10 based zoning for network storage security applications. Other innovative, unique features include DataBolt Bandwidth Optimizer. DataBolt Bandwidth Optimizer is designed to help facilitate the industry transition to 12Gb/s SAS-enabled systems by allowing users to take advantage of 12Gb/s speeds while utilizing existing 6Gb/s SATA drives and backplanes. The expander buffers 3Gb/s or 6Gb/s data from the end device and then transfers it out to the host at 12Gb/s speeds in order to match the bandwidth between faster hosts and slower SATA devices. Additional features such as Port Mirroring allow the customer to access signals that are not easily accessible by mirroring that signal to an external connection for debug and diagnosis. The SATA affiliation option allows for turning off all affiliations and allowing multiple hosts to control various SATA drives.

For improving system reliability for today's data center and storage subsystems, the LSI SAS expanders are ideal for connecting multiple targets and hosts in a SAS switched environment. This will support scalability and fault-tolerant path redundancy.

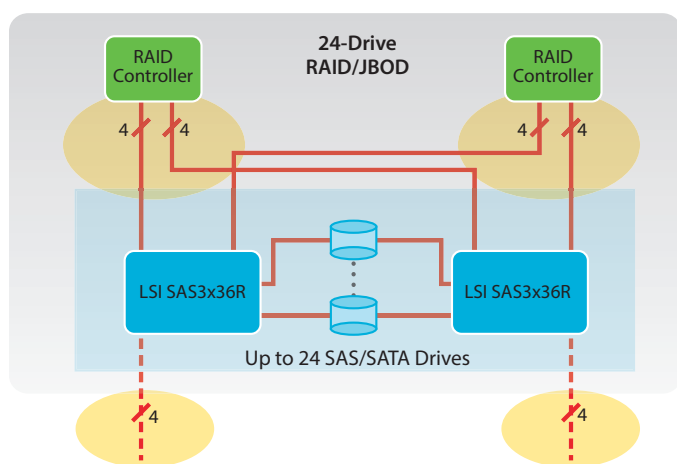
### SAS Leadership

LSI is a storage market leader in SAS, Enterprise RAID, and Application Acceleration, enabling the entire storage ecosystem. LSI offers the broadest product portfolio in the industry including SAS ROCs, IO Controllers, Expanders, Host Bus Adapters, MegaRAID® controller cards, switches, advanced software options, and application acceleration solutions. LSI solutions provide the performance, connectivity, scalability, and manageability required for today's enterprise storage environments.

These products use the LSI proven SAS core which has completed extensive stress test and interoperability testing resulting in the industry's most robust, interoperable solution.

## SDK

A full Software Development Kit (SDK) is available to help enable efficient OEM specific firmware implementations or feature-rich, out of the box operation. The SDK includes source code for the SES module with examples of typical SES device drivers, the SSP Target, the ISTWI Target, the CLI Interface and the Serial Debug port. API support is also included for most HAL functions including, but not limited to, SSP and SMP Target and Initiator, STP Initiator, UART, ISTWI Master and Slave, all LED management including GPIO and SGPIO, Configuration Page Management, PHY configuration and control, firmware event logging and firmware tracing.



### Enclosure Functions

- LED Control
- Temp Monitoring
- Drive/Array Control
- Fan Control/Monitor
- Power Supply Control/Monitor
- Alarms/Notifications

## LSI SAS3x36R Series SAS Expander Technical Specifications

SEP	ARM Cortex R4 300MHz	A/D Converter	1, (2 analog inputs)
Instructions	1.6 DMIPs/MHz	Pulse Width Modulator	2
Internal Memory	2MB SRAM	External Memory Interfaces	8/16-bit Flash, with 6 chip selects SRAM, NVSRAM
SAS Ports	36, 28, 24	Security Features	TRNG/Hash & HMAC Generator/Secret Root Key
Data Transfer	12.0/6.0/3.0 Gb/s SAS 6.0/3.0 Gb/s SATA	8-bit SEP interface	Y
DataBolt Bandwidth Optimizer (EDFB)	All ports (LSI IP)	I2C Interfaces	5
SSC (Spread Spectrum Clocking)	Y	SGPIO interface	2, SFF- 8485
Port Mirroring	Y (LSI IP)	UART Interfaces	2
Direct, Table, & Subtractive Routing Methods	Y	LED	1 LED pin per PHY
Route Table Entries	2048	GPIO	16
Phy Based Zoning, T-10 Zoning	Y	Package	672 FPB32GU 1.0 mm pad pitch (27mm x 27mm)
T-10 Optical Support	Y		